

Design technology for heterogeneous embedded systems

Deskripsi Lengkap: <https://lib.ui.ac.id/detail?id=20398193&lokasi=lokal>

Abstrak

Heterogeneous embedded systems, signal representations and different abstraction levels, architectures and components based on hardware and software, in all the main phases of design (modeling, validation with multiple models of computation, synthesis and optimization). It concentrates on the specific issues at the interfaces, and is divided into two main parts. The first part examines mainly theoretical issues and focuses on the modeling, validation and design techniques themselves. The second part illustrates the use of these methods in various design contexts at the forefront of new technology and architectural developments.