

Guide to FPGA implementation of arithmetic functions

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Deskripsi Lengkap: <https://lib.ui.ac.id/detail?id=20398338&lokasi=lokal>

Abstrak

This is not a book on algorithms. It is a book that shows how to translate efficiently an algorithm to a circuit, using techniques such as parallelism, pipeline, loop unrolling, and others. Numerous examples of FPGA implementation are described throughout this book and the circuits are modeled in VHDL. Complete and synthesizable source files are available for download.