

Syndevs co-design flow: a hardware / software co-design flow based on the discrete event system specification model of computation

Molter, H. Gregor, author

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Abstrak

H. Gregor Molter develops a hardware / software co-design flow based on the Discrete Event System Specification model of computation. He advocates that such a system level design flow should exploit a timed model of computation to allow a broad application field. The presented design flow will transform timed DEVS models to both synthesizable VHDL source code and embeddable C++ source code.