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Beyond-CMOS Technologies for Next Generation Computer Design

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Abstrak

This book describes the bottleneck faced soon by designers of traditional CMOS devices, due to device scaling, power and energy consumption, and variability limitations. This book aims at bridging the gap between device technology and architecture/system design. Readers will learn about challenges and opportunities presented by "beyond-CMOS devices" and gain insight into how these might be leveraged to build energy-efficient electronic systems.