

Ultra low power ECG processing system for IoT devices

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Deskripsi Lengkap: <https://lib.ui.ac.id/detail?id=9999920521458&lokasi=lokal>

Abstrak

This book describes an ECG processing architecture that guides biomedical SoC developers, from theory to implementation and testing. The authors provide complete coverage of the digital circuit implementation of an ultra-low power biomedical SoC, comprised of a detailed description of an ECG processor implemented and fabricated on chip. Coverage also includes the challenges and tradeoffs of designing ECG processors.

- Describes digital circuit architecture for implementing ECG processing algorithms on chip;
- Includes coverage of signal processing techniques for ECG processing;
- Features ultra-low power circuit design techniques;
- Enables design of ECG processing architectures and their respective on-chip implementation.