## LAMPIRAN









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## Optical Shaft Encoder

## Description:

The $\mathbf{S 5}$ series optical shaft encoder is a non-contacting rotary to digital converter. Useful for position feedback or manual interface, the encoder converts real-time shaft angle, speed, and direction into TTL-compatible quadrature outputs with or without index. The encoder utilizes an unbreakable mylar disk, metal shaft and bushing, LED light source, and monolithic electronics. It operates from a single +5 VDC supply.

Three shaft torque versions are available. The standard torque version and M6-option have a sleeve bushing lubricated with a viscous motion control gel to provide torque and a feel that is ideal for front panel human interface applications.

The NT-option (no torque added) has a sleeve bushing and a low viscosity lubricant (that does not intentionally add torque) for low RPM applications where a small amount of torque is acceptable.

The B-option and BM6-option have a ball bearing rather than a sleeve bushing for high speed, free spinning, and zero torque applications. The ball bearing options are recommended when a pulley, gear, or friction wheel drives the shaft. This eliminates the wear that would otherwise result from the side load even at slow speeds.

A secure connection to the $\mathbf{S} 5$ series encoder is made through a 5 -pin (single-ended version) or $10-\mathrm{pin}$ (differential version) finger-latching connector (sold separately). The mating connectors are available from US Digital with several cable options and lengths.

For differential version: the internal differential line driver (26C31) can source and sink 20 mA at TTL levels. The recommended receiver is industry standard 26C32. Maximum noise immunity is achieved when the differential receiver is terminated with a 110 ohm resistor in series with a $.0047 \mu \mathrm{f}$ capacitor placed across each differential pair. The capacitor simply conserves power; otherwise power consumption would increase by approximately 20 mA per pair, or 60 mA for 3 pairs.

## Features:

> Small size
> Low cost
> Optional Agilent compatible pin-out
> Optional differential / line-driver output
>Positive finger-latching connector
>2-channel quadrature,TTL squarewave outputs
$>$ 3rd channel index option
> Tracks from 0 to 100,000 cycles/sec
> Ball bearing option tracks to 10,000 RPM
$>-40$ to $+100^{\circ} \mathrm{C}$ operating temperature
> Single +5 VDC supply
> US Digital warrants its products against defects in materials and workmanship for two years. See complete warranty for details.

## Differential Electrical Specifications:

| Parameter | Min. | Typ. | Max. | Units | Notes |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Supply | 4.5 | 5.0 | 5.5 | Volts |  |
| Current Consumption |  |  |  |  |  |
| $\quad$ Index: 32 CPR | - | 28 | 53 | mA | No load |
| Index: 720, 900, 1000, 1250 CPR | - | 56 | 59 | mA | No load |
| Index: All Other Resolutions | - | 58 | 88 | mA | No load |
| Non-index: <2000 CPR | - | 18 | 43 | mA | No load |
| Non-index: >=2000 CPR | - | 58 | 88 | mA | No load |
| Output Voltage |  |  |  | Volts | @ -20mA |
| Sourcing to +5 | 2.4 | 3.4 | - | Volts | @ 20mA |
| Sinking to Ground | - | 0.2 | 0.4 | Volts |  |
| Por complete details see the EM1 / HEDS data sheet. |  |  |  |  |  |

## Single-ended Electrical Specifications:

For complete details see the EM1 / HEDS data sheet.

## Phase Relationship:

$B$ leads $A$ for clockwise shaft rotation, and $A$ leads $B$ for counterclockwise rotation viewed from the shaft side of the encoder (see the EM1 / HEDS data sheet).

Single-ended (S5S)


Differential (S5D)


Single-ended Mechanical Drawings (S5S):


Differential Mechanical Drawings (S5D):


## Compatible Cables / Connectors:

Finger-latching:

| 5-pin | 10-pin | Description |
| :--- | :--- | :--- |
| CON-FC5-22* | CON-FC10 | Connector |
| CA-3133-1FT** | - | Connector on one end with 4 12" wires |
| CA-3132-1FT** | - | Connector on one end with 5 12" wires <br> CA-3131-6FT** <br> CA-4217-6FT |
| Connector on one end of a 6' shielded <br> round cable |  |  |
| - | CA-4174-6FT*** | Same as CA-4217, but for L-option only <br> CA-3620-6FT** <br> CA-3619-6FT |
| Connectors on both ends of a 6' <br> shielded round cable |  |  |
| - | CA-3807-FT*** | Same as CA-3807, but for L-option only |

* 22 AWG is standard. 24, 26 and 28 AWG are also available.
** Single-ended output and accompanying cables are typically designed for cable lengths of 6 feet or less; for longer cable lengths, differential output and accompanying cables are recommended.
*** Avago / Agilent / HP compatible cable assembly.


## Attention:

$>$ Specify cable length when ordering.
$>$ Custom cable lengths are available. See the Cables / Connectors data sheet for more information.

## Pin-outs:

| Pin | 5-pin <br> Single-ended | 10-pin Differential <br> Standard | 10-pin Differential <br> Avago (L-option) |
| :--- | :--- | :--- | :--- |
| 1 | Ground | Ground | No connection |
| 2 | Index | Ground | +5VDC power |
| 3 | A channel | Index- | Ground |
| 4 | +5VDC power | Index | No connection |
| 5 | B channel | A- channel | A- channel |
| 6 |  | A+ channel | A+ channel |
| 7 |  | +5VDC power | B- channel |
| 8 | +5VDC power | B+ channel |  |
| 9 | B- channel | Index- |  |
| 10 | B+ channel | Index+ |  |

## Ordering Information:

| S5S <br> Standard: | S5S <br> Index/HiRes: <br> (Hi Res: >=1000 CPR) | S5D |
| :--- | :--- | :--- |
| Standard: |  |  |

## S5D

Index/HiRes:
(Hi Res: >=1000 CPR) >Add \$5 for M6-option.
$\$ 74.87 / 1>$ Add $\$ 13$ for BM6-option.


Options: (specify in order shown)
Blank (default) $=1 / 4^{\prime \prime}$ dia. sleeve bushing (standard torque).
$\mathbf{I}=$ Index (3rd channel).
$\mathrm{L}=$ Avago / Agilent / HP compatible pin-out.***
$B=1 / 4^{\prime \prime}$ dia. ball bearing (free spinning).
M6 $=6 \mathrm{~mm}$ dia. sleeve bushing (standard torque).
BM6 $=6 \mathrm{~mm}$ dia. ball bearing (free spinning).**
NT = Replaces standard torque with no torque added.

Notes:

* Index option not available.
** $32,720,900,1000,1250$ CPR only available with index.
${ }_{* * * * *}$ Only available with differential version (S5D).
**** Not available with NT-option (no torque added).
Technical Data, Rev. 06.11.07, June 2007 All information subject to change without notice.


## Start Here:

>Use charts below to determine which module family your application uses (based on CPR/CPI).

## 1" Resolutions:

| CPR | Non-Index | With Index |
| :--- | :--- | :--- |
| $\mathbf{3 2}$ | n/a | EM1 |
| $\mathbf{5 0}$ | HEDS | HEDS |
| $\mathbf{9 6}$ | HEDS | HEDS |
| $\mathbf{1 0 0}$ | HEDS | HEDS |
| $\mathbf{1 1 0}$ | HEDS | n/a |
| $\mathbf{1 2 0}$ | HEDS | n/a |
| $\mathbf{1 9 2}$ | HEDS | HEDS |
| $\mathbf{2 0 0}$ | HEDS | HEDS |
| $\mathbf{2 5 0}$ | HEDS | HEDS |
| $\mathbf{2 5 6}$ | HEDS | HEDS |
| $\mathbf{3 6 0}$ | HEDS | HEDS |
| $\mathbf{4 0 0}$ | HEDS | HEDS |
| $\mathbf{5 0 0}$ | HEDS | HEDS |
| $\mathbf{5 1 2}$ | HEDS | HEDS |
| $\mathbf{5 4 0}$ | HEDS | n/a |
| $\mathbf{7 2 0}$ | n/a | EM1 |
| $\mathbf{9 0 0}$ | n/a | EM1 |
| $\mathbf{1 0 0 0}$ | HEDS | EM1 |
| $\mathbf{1 0 1 6}$ | HEDS | n/a |
| $\mathbf{1 0 2 4}$ | HEDS | EM1 |
| $\mathbf{1 2 5 0}$ | n/a | EM1 |

## 2" Resolutions:

| CPR | Non-Index | With Index |
| :--- | :--- | :--- |
| $\mathbf{6 4}$ | n/a | EM1 |
| $\mathbf{1 0 0}$ | HEDS | HEDS |
| $\mathbf{2 0 0}$ | HEDS | HEDS |
| $\mathbf{4 0 0}$ | HEDS | HEDS |
| $\mathbf{5 0 0}$ | HEDS | HEDS |
| $\mathbf{5 1 2}$ | HEDS | n/a |
| $\mathbf{1 0 0 0}$ | HEDS | HEDS |
| $\mathbf{1 0 2 4}$ | HEDS | HEDS |
| $\mathbf{1 8 0 0}$ | n/a | EM1 |
| $\mathbf{2 0 0 0}$ | HEDS | HEDS |
| $\mathbf{2 0 4 8}$ | HEDS | HEDS |
| $\mathbf{2 5 0 0}$ | EM1 | EM1 |

## Linear Strip Resolutions:

| CPI | Non-Index | With Index |
| :--- | :--- | :--- |
| $\mathbf{1 2 0}$ | n/a | EM1 |
| $\mathbf{1 2 5}$ | n/a | EM1 |
| $\mathbf{1 2 7}$ | n/a | EM1 |
| $\mathbf{1 5 0}$ | n/a | EM1 |
| $\mathbf{1 8 0}$ | HEDS | n/a |
| $\mathbf{2 0 0}$ | n/a | EM1 |
| $\mathbf{2 5 0}$ | n/a | EM1 |
| $\mathbf{3 0 0}$ | HEDS | n/a |
| $\mathbf{3 6 0}$ | HEDS | n/a |
| $\mathbf{5 0 0}$ | n/a | EM1 |

## Features:

> Two channel quadrature output with index pulse > No signal adjustment
> TTL Compatible

- Single +5 V supply
> The EM1 and HEDS are both RoHS compliant
> US Digital warrants its products against defects and workmanship for two years. See complete warranty for details.


## EM1:

- Resolutions up to 2500 CPR ( 10,000 PPR)
> Internal 0.1 ufd bypass capacitor
$>-55^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ operating temperature


## HEDS:

> Resolutions up to 2048 CPR (8192 PPR)
$>-40^{\circ} \mathrm{C}$ to $100^{\circ} \mathrm{C}$ operating temperature

## Description:

The EM1 and HEDS products are transmissive optical encoder modules. These modules are designed to detect rotary or linear position when used together with a codewheel or linear strip. The EM1 and HEDS modules consist of a lensed LED source and a monolithic detector IC enclosed in a small polymer package. These modules use phased array detector technology to provide superior performance and greater tolerances over traditional aperture mask type encoders.

Both the EM1 and HEDS module provide digital quadrature outputs. The EM1 comes standard with a third index channel output on all resolutions. The HEDS is available with a third index channel output on only some resolutions.

The EM1 and HEDS transmissive optical encoder modules are powered from a single +5 VDC power supply. Additional power supply voltages for the EM1 will be available in the near future. The EM1 single-ended outputs are capable of sinking or sourcing 8 mA each.

The resolution of the modules and encoder disks or linear strips must match. Two mounting holes are provided to accept screws up to .105" dia. Both the EM1 and HEDS have identical mounting and pin-out configurations.

For open collector and higher voltage applications, add the PC3 device (see the PC3 data sheet), or for differential cable driver outputs, add the PC4 device (see the PC4 data sheet). Encoder disks, linear strips, quadrature decoder chips, counter chips, computer interface boards, mating connectors and cables are also available.

The EM1 and HEDS are both RoHS compliant.

## EM1 Mechanical Drawing:



HEDS Mechanical Drawing:


## EM1 / HEDS

## EM1 Module \& Disk Mechanical Alignment:



HEDS Module \& Disk Mechanical Alignment:


EM1 Module \& Linear Strip Mechanical Alignment:


## HEDS Module \& Linear Strip Mechanical Alignment:



## Encoding Characteristics:

> Specifications apply over entire operating temperature range.
Values are for the worst error over a full rotation.
$>$ Refer to Timing Diagram on next page.

| Parameter | Symbol | Min. | Typ. | Max. Units |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Cycle Error |  |  |  |  |  |
| HEDS (2000 or 2048 CPR only) |  |  | 3.0 | 7.5 | ${ }^{\circ} \mathrm{e}$ |
| EM1 \& HEDS (All Other Resolutions) |  |  | 3.0 | 5.5 | ${ }^{\circ} \mathrm{e}$ |
| Symmetry |  |  |  |  |  |
| HEDS (2000 or 2048 CPR only) |  | 130 | 180 | 230 | ${ }^{\circ} \mathrm{e}$ |
| EM1 \& HEDS (All Other Resolutions) |  | 150 | 180 | 210 | ${ }^{\circ} \mathrm{e}$ |
| Quadrature |  |  |  |  |  |
| HEDS (2000 or 2048 CPR only) |  | 40 | 90 | 140 | ${ }^{\circ} \mathrm{e}$ |
| EM1 \& HEDS (All Other Resolutions) |  | 60 | 90 | 120 | e |
| Index Pulse Width |  |  |  |  |  |
| HEDS (2000 or 2048 CPR only) | Po | 40 | 90 | 140 | ${ }^{\circ} \mathrm{e}$ |
| EM1 \& HEDS (All Other Resolutions) | Po | 60 | 90 | 120 | ${ }^{\circ} \mathrm{e}$ |
| Ch. I Rise After Ch. B or Ch. A Fall |  |  |  |  |  |
| EM1 | t1 | 10 | 100 | 250 | ns |
| HEDS (2000 or 2048 CPR only) | t1 | 10 | 450 | 1500 | ns |
| HEDS (All Other Resolutions) | t1 | -300 | 100 | 250 | ns |
| Ch. I Fall After Ch. A or Ch. B Rise |  |  |  |  |  |
| EM1 | t2 | 70 | 150 | 300 | ns |
| HEDS (2000 or 2048 CPR only) | t2 | 10 | 250 | 1500 | ns |
| HEDS (All Other Resolutions) | t2 | 70 | 150 | 1000 | ns |

## Recommended Operating Conditions:

| Parameter | Min. Max. Units | Notes |  |  |
| :--- | :--- | :--- | :--- | :--- |
| Temperature |  |  |  |  |
| $\quad$ EM1 | -55 | 125 | ${ }^{\circ} \mathrm{C}$ |  |
| $\quad$ HEDS | -40 | 100 | ${ }^{\circ} \mathrm{C}$ |  |
| Supply Voltage | 4.5 | 5.5 | Volts | Ripple $<100 \mathrm{mV} \mathrm{P}_{\text {P-p }}$ |
| Load Capacitance | - | 100 | pF |  |
| Count Frequency | - | 100 | kHz | $\mathrm{rpm} / 60 \times$ cycles $/ \mathrm{rev}$. |

## Electrical Specifications:

>Specifications apply over entire operating temperature range.
Typical values are specified at Vcc $=5.0 \mathrm{~V}$ and $25^{\circ} \mathrm{C}$.
$>$ Refer to Timing Diagram on next page.

| Parameter | Min. | Typ. | Max. | Units | Notes |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Output Voltage | -0.5 | - | Vcc | Volts |  |
| Supply Current |  |  |  |  |  |
| EM1 (32 thru 250 CPR) | - | 27 | 30 | mA |  |
| EM1 (All Other Resolutions) | - | 55 | 57 | mA |  |
| HEDS (Index or $1^{\prime \prime}>=1000$ CPR or 2 " >= 2000 CPR only) | 30 | 57 | 85 | mA |  |
| HEDS (Non-index or All Other Resolutions) | - | 17 | 40 | mA |  |
| Output Low* |  |  |  |  |  |
| EM1 | - | - | 0.5 | Volts | $\mathrm{I}_{\mathrm{OL}}=8.0 \mathrm{~mA}$ max. |
| HEDS (Index or $1^{\prime \prime}>=1000$ CPR or 2 " >= 2000 CPR only) | - | - | 0.4 | Volts | $\mathrm{I}_{\mathrm{LL}}=3.86 \mathrm{~mA}$ max. |
| HEDS (Non-index or All Other Resolutions) | - | - | 0.4 | Volts | $\mathrm{I}_{\mathrm{OL}}=3.2 \mathrm{~mA}$ max. |
| Output High* |  |  |  |  |  |
| EM1 | 2.0 | - | - | Volts | $\mathrm{I}_{\mathrm{OH}}=-8.0 \mathrm{~mA}$ max. |
| HEDS (Index or $1^{\prime \prime}>=1000$ CPR or $2^{\prime \prime}>==2000$ CPR only) | 2.4 | - | - | Volts | $\mathrm{I}_{\text {OH }}=-200 \mu \mathrm{~A}$ max. |
| HEDS (Non-index or All Other Resolutions) | 2.4 | - | - | Volts | $\mathrm{I}_{\mathrm{OH}}=-40 \mu \mathrm{~A}$ max. |
| Output Current Per Channel |  |  |  |  |  |
| EM1 | -8.0 | - | 8.0 | mA |  |
| HEDS | -1.0 | - | 5.0 | mA |  |

* Unloaded high level output voltage is 4.80 V typically, 4.2 V minimum.


## Phase Relationship:

For Shaft Encoders: (View the encoder so the shaft / bushing side is facing up.)
$>$ A leads $B$ in a clockwise rotation; $B$ leads $A$ in a counterclockwise rotation for the following products: $>B$ leads $A$ in a clockwise rotation; A leads $B$ in a counterclockwse rotation for the following products:

For Kit Encoders: (View the encoder so the cover side is facing up.)
$>$ A leads B in a clockwise rotation; B leads A in a counterclockwise rotation for the following products:
$>B$ leads A in a clockwise rotation; A leads B in a counterclockwise rotation for the following products:

## For Probe Encoders:

> A leads B in inward plunger motion; B leads A in outward plunger motion for the following products:
For Inclinometers: (View the inclinometer so the cover side is facing up.)
>A leads B in a clockwise rotation; B leads A in a counterclockwise rotation for the following products:

H1.
H15, H3, H5, H6, HB5M, HB6M, HD25, S1,
S2, S5, S6 and SP-16.
E3, E5 and E6.
E2.

PE.

T5 and T6.

## Timing Diagram:



CPR ( $\mathbf{N}$ ): The number of Cycles Per Revolution.
One Shaft Rotation: 360 mechanical degrees, N cycles.
One Electrical Degree ( ${ }^{\circ} \mathrm{e}$ ): $1 / 360$ th of one cycle.
One Cycle (C): 360 electrical degrees ( ${ }^{\circ}$ e). Each cycle can be decoded into 1 or 4 codes, referred to as X1 or X4 resolution multiplication.
Symmetry: A measure of the relationship between $(\mathrm{X})$ and $(\mathrm{Y})$ in electrical degrees, nominally $180^{\circ} \mathrm{e}$.
Quadrature (Z): The phase lag or lead between channels A and B in electrical degrees, nominally $90^{\circ} \mathrm{e}$.
Index (CH I.): The index output goes high once per revolution, coincident with the low states of channels $A$ and $B$, nominally $1 / 4$ of one cycle $\left(90^{\circ} \mathrm{e}\right)$.
Position Error: The difference between the actual shaft position and the position indicated by the encoder cycle count.
Cycle Error: An indication of cycle uniformity. The difference between an observed shaft angle which gives rise to one electrical cycle, and the nominal angular increment of $1 / \mathrm{N}$ of a revolution.

## EM1 / HEDS Encoder Module Differences:

US Digital is the designer and manufacturer of the EM1 transmissive optical encoder module. The design of the EM1 provides electrical and mechanical compatibility with the Agilent HEDS-9000, HEDS-9100, HEDS-9200, HEDS-9040, and HEDS-9140 series modules. Non-index codewheels are interchangable between the EM1 and HEDS modules. The process of switching from the HEDS to the EM1 module should not require any mechanical or electrical changes. Simply use the EM1 and matching codewheel in place of the HEDS module and codewheel.

The EM1 has a built in index channel and is available on all resolutions, for both rotary disks and linear strips. The EM1 offers improved output drive capability and will source and sink 8 mA at TTL levels. The current consumption is reduced over Agilent index versions ( 27 mA vs. 57 mA typical). Physically the EM1 has no external wire loops which interfere when mounting. The connector pins are 0.051 " shorter than Agilent, while still providing .30 insertion depth. The EM1 uses a US Digital designed codewheel with 2 tracks rather than 3 tracks for index versions. US Digital's EM1 offers custom and special resolutions.

## EM1 / HEDS

## Ordering Information:

$>$ The part numbers below do not include optical encoder disks or linear strips.
$>$ Disks and linear strips must be ordered separately (see the DISK or LIN data sheet).

| Pricing Levels per Module for 1" Disks: |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| CPR | Non-Index Part Number | Pricing | Level | With Index Part Number | Pricing Level |
| 32 | - | - |  | EM1-1-32 | 2 |
| 50 | HEDS-9100-S00 | 1 |  | HEDS-9140-S00 | 2 |
| 96 | HEDS-9100-C00 | 1 |  | HEDS-9140-C00 | 2 |
| 100 | HEDS-9100-C00 | 1 |  | HEDS-9140-C00 | 2 |
| 110 | HEDS-9100-C00 | 1 |  | - | - |
| 120 | HEDS-9100-C00 | 1 |  | - | - |
| 192 | HEDS-9100-E00 | 1 |  | HEDS-9140-E00 | 2 |
| 200 | HEDS-9100-E00 | 1 |  | HEDS-9140-E00 | 2 |
| 250 | HEDS-9100-F00 | 1 |  | HEDS-9140-F00 | 2 |
| 256 | HEDS-9100-F00 | 1 |  | HEDS-9140-F00 | 2 |
| 360 | HEDS-9100-G00 | 1 |  | HEDS-9140-G00 | 2 |
| 400 | HEDS-9100-H00 | 1 |  | HEDS-9140-H00 | 2 |
| 500 | HEDS-9100-A00 | 1 |  | HEDS-9140-A00 | 2 |
| 512 | HEDS-9100-100 | 1 |  | HEDS-9140-100 | 2 |
| 540 | HEDS-9100-100 | 1 |  |  | - |
| 720 | - | - |  | EM1-1-720 | 3 |
| 900 | - | - |  | EM1-1-900 | 3 |
| 1000 | HEDS-9100-B00 | 2 |  | EM1-1-1000 | 3 |
| 1016 | HEDS-9100-J00 | 2 |  | - | - |
| 1024 | HEDS-9100-J00 | 2 |  | EM1-1-1024 | 3 |
| 1250 | - | - |  | EM1-1-1250 | 3 |

Pricing Levels per Module for 2" Disks:

| CPR | Non-Index <br> Part Number | Pricing Level | With Index <br> Part Number | Pricing Level |
| :--- | :--- | :--- | :--- | :--- |
| $\mathbf{6 4}$ | - | - | EM1-2-64 | 2 |
| $\mathbf{1 0 0}$ | HEDS-9100-S00 | 1 | HEDS-9140-S00 | 2 |
| $\mathbf{2 0 0}$ | HEDS-9100-C00 | 1 | HEDS-9140-C00 | 2 |
| $\mathbf{4 0 0}$ | HEDS-9100-E00 | 1 | HEDS-9140-E00 | 2 |
| $\mathbf{5 0 0}$ | HEDS-9000-A00 | 1 | HEDS-9140-F00 | 2 |
| $\mathbf{5 1 2}$ | HEDS-9000-A00 | 1 | - | - |
| $\mathbf{1 0 0 0}$ | HEDS-9000-B00 | 1 | HEDS-9040-B00 | 2 |
| $\mathbf{1 0 2 4}$ | HEDS-9000-J00 | 1 | HEDS-9040-J00 | 2 |
| $\mathbf{1 8 0 0}$ | - | EM1-2-1800 | 3 |  |
| $\mathbf{2 0 0 0}$ | HEDS-9000-T00 | - | HEDS-9040-T00 | 2 |
| $\mathbf{2 0 4 8}$ | HEDS-9000-U00 | 2 | HEDS-9040-T00 | 2 |
| $\mathbf{2 5 0 0}$ | EM1-2-2500-N | 3 | EM1-2-2500 | 3 |

Pricing Levels per Module for Linear Strips:

| CPR | Non-Index <br> Part Number | Pricing Level | With Index <br> Part Number | Pricing Level |
| :--- | :--- | :--- | :--- | :--- |
| $\mathbf{1 2 0}$ | - | - | EM1-0-120 | 2 |
| $\mathbf{1 2 5}$ | - | - | EM1-0-125 | 2 |
| $\mathbf{1 2 7}$ | - | - | EM1-0-127 | 2 |
| $\mathbf{1 5 0}$ | - | EM1-0-150 | 2 |  |
| $\mathbf{1 8 0}$ | HEDS-9200-Q00 | - | - | - |
| $\mathbf{2 0 0}$ | - | - | EM1-0-200 | 2 |
| $\mathbf{2 5 0}$ | - | - | - | 2 |
| $\mathbf{3 0 0}$ | HEDS-9200-300 | 2 | - | - |
| $\mathbf{3 6 0}$ | HEDS-9200-360 | 2 | EM1-0-500 | 4 |
| $\mathbf{5 0 0}$ | - | - |  |  |

LM123/LM223
LM323

## THREE-TERMINAL 3A-5V POSITIVE VOLTAGE REGULATORS

- OUTPUT CURRENT: 3A
- INTERNAL CURRENT AND THERMAL LIMITING
■ TYPICAL OUTPUT IMPEDANCE: $0.01 \Omega$
- MINIMUM INPUT VOLTAGE: 7.5V
- POWER DISSIPATION: 30W


## DESCRIPTION

The LM123, LM223, LM323 are three-terminal positive voltage regulators with a preset 5 V output and a load driving capability of 3A. New circuit design and processing techniques are used to provide the high output current without sacrificing the regulation characteristics of lower current devices.
The 3A regulator is virtually blowout proof.
Current limiting, power limiting and thermal shut-down provide the same high level of reliability obtained with these techniques in the LM209, 1A regulator. An overall worst case specification for the combined effects of input voltage, load current, ambient temperature, and power

dissipation ensure that the LM123, LM223, LM323 will perform satisfactorily as a system element.

SCHEMATIC DIAGRAM


## ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter $^{2}$ | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{I}}$ | Input Voltage | 20 | V |
| $\mathrm{I}_{\mathrm{O}}$ | Output Current | Internally Limited |  |
| $\mathrm{P}_{\text {tot }}$ | Power Dissipation | Internally Limited |  |
| $\mathrm{T}_{\text {stg }}$ | Storage Temperature Range | -65 to 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {oper }}$ | Operating Junction Temperature Range | LM123 | -55 to 150 |
|  |  | C |  |
|  |  | LM223 | -25 to 125 |

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

## THERMAL DATA

| Symbol | Parameter | TO-220 | TO-3 | Unit |
| :---: | :--- | :---: | :---: | :---: |
| $\mathrm{R}_{\text {thj-case }}$ | Thermal Resistance Junction-case Max | 3 | 2 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{R}_{\text {thj-amb }}$ | Thermal Resistance Junction-ambient Max | 50 | 35 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

CONNECTION DIAGRAM (top view)


## ORDERING CODES

| TYPE | TO-220 | TO-3 | TEMPERATURE RANGE |
| :---: | :---: | :---: | :---: |
| LM123 |  | LM123K | $-55^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ |
| LM223 |  | LM223K | $-25^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ |
| LM323 | LM323T | LM323K | $0^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ |

ELECTRICAL CHARACTERISTICS OF LM123/LM223 ( $T_{J}=-55$ to $150^{\circ} \mathrm{C}$ for LM123, $T_{J}=-25$ to $150^{\circ} \mathrm{C}$ for LM223 unless otherwise specified).

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{O}}$ | Output Voltage Range (Note 2) | $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}, \quad \mathrm{V}_{\mathrm{l}}=7.5 \mathrm{~V}, \quad \mathrm{I}_{\mathrm{O}}=0$ | 4.7 | 5 | 5.3 | V |
| $\mathrm{V}_{\mathrm{O}}$ | Output Voltage Range (Note 2) | $\begin{array}{ll} \hline \mathrm{T}_{\mathrm{J}}=\mathrm{T}_{\text {min }} \text { to } \mathrm{T}_{\max } & \mathrm{P} \leq \mathrm{P}_{\max } \\ \mathrm{V}_{\mathrm{I}}=7.5 \text { to } 15 \mathrm{~V} & \mathrm{I}_{\mathrm{O}}=0 \text { to } 3 \mathrm{~A} \end{array}$ | 4.6 |  | 5.4 | V |
| $\mathrm{K}_{\mathrm{VI}}$ | Line Regulation (Note 3) | $\mathrm{V}_{1}=7.5$ to $15 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 5 | 25 | mV |
| $\mathrm{K}_{\mathrm{Vo}}$ | Load Regulation (Note 3) | $\mathrm{I}_{\mathrm{O}}=0$ to $3 \mathrm{~A} \mathrm{~V}_{1}=7.5 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 25 | 100 | mV |
| $\mathrm{I}_{\mathrm{B}}$ | Quiescent Current | $\mathrm{V}_{\mathrm{I}}=7.5$ to $15 \mathrm{~V} \quad \mathrm{I}_{\mathrm{O}}=0$ to 3 A |  | 12 | 20 | mA |
| $\mathrm{V}_{\mathrm{NO}}$ | Output Noise Voltage | $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C} \quad \mathrm{f}=10 \mathrm{~Hz}$ to 100 KHz |  | 40 |  | $\mu \mathrm{V}_{\text {rms }}$ |
| los | Short Circuit Current Limit | $\mathrm{V}_{1}=15 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 3 | 4.5 | A |
|  |  | $\mathrm{V}_{1}=7.5 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 4 | 5 |  |
| $\mathrm{K}_{\mathrm{VH}}$ | Long Term Stability | $\mathrm{V}_{1}=7.5 \mathrm{~V} \mathrm{~T}^{2}-25{ }^{\text {d }}$ |  |  | 35 | mV |

Notes: 1. Although power dissipation is internally limited, specifications apply only for $\mathrm{P} \leq 30 \mathrm{~W}$.
2. Selected devices with tightened tolerance output voltage available.
3. Load and line regulation are specified at constant junction temperature. Pulse testing is required with a pulse width $\leq 1 \mathrm{~ms}$ and duty cycle $\leq 5 \%$.

ELECTRICAL CHARACTERISTICS OF LM323 ( $T_{J}=0$ to $150^{\circ} \mathrm{C}$, unless otherwise specified).

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{O}}$ | Output Voltage Range (Note 2) | $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}, \quad \mathrm{V}_{\mathrm{l}}=7.5 \mathrm{~V}, \quad \mathrm{I}_{\mathrm{O}}=0$ | 4.8 | 5 | 5.2 | V |
| $\mathrm{V}_{\mathrm{O}}$ | Output Voltage Range (Note 2) | $\begin{array}{ll} \hline \mathrm{T}_{\mathrm{J}}=\mathrm{T}_{\min } \text { to } \mathrm{T}_{\max } & \mathrm{P} \leq \mathrm{P}_{\max } \\ \mathrm{V}_{\mathrm{I}}=7.5 \text { to } 15 \mathrm{~V} & \mathrm{I}_{\mathrm{O}}=0 \text { to } 3 \mathrm{~A} \end{array}$ | 4.75 |  | 5.25 | V |
| $\mathrm{K}_{\mathrm{VI}}$ | Line Regulation (Note 3) | $\mathrm{V}_{1}=7.5$ to $15 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 5 | 25 | mV |
| $\mathrm{K}_{\mathrm{vo}}$ | Load Regulation (Note 3) | $\mathrm{I}_{\mathrm{O}}=0$ to $3 \mathrm{~A} \quad \mathrm{~V}_{1}=7.5 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 25 | 100 | mV |
| $\mathrm{I}_{\mathrm{IB}}$ | Quiescent Current | $\mathrm{V}_{\mathrm{I}}=7.5$ to $15 \mathrm{~V} \quad \mathrm{I}_{\mathrm{O}}=0$ to 3 A |  | 12 | 20 | mA |
| $\mathrm{V}_{\mathrm{NO}}$ | Output Noise Voltage | $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C} \quad \mathrm{f}=10 \mathrm{~Hz}$ to 100 KHz |  | 40 |  | $\mu \mathrm{V}_{\text {rms }}$ |
| los | Short Circuit Current Limit | $\mathrm{V}_{1}=15 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 3 | 4.5 | A |
|  |  | $\mathrm{V}_{1}=7.5 \mathrm{~V} \quad \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |  | 4 | 5 |  |
| $\mathrm{K}_{\mathrm{VH}}$ | Long Term Stability |  |  |  | 35 | mV |

Notes: 1. Although power dissipation is internally limited, specifications apply only for $\mathrm{P} \leq 30 \mathrm{~W}$.
2. Selected devices with tightened tolerance output voltage available.
3. Load and line regulation are specified at constant junction temperature. Pulse testing is required with a pulse width $\leq 1 \mathrm{~ms}$ and duty cycle $\leq 5 \%$.

Figure 1 : Output Noise Voltage


Figure 2 : Output Impedance


Figure 3 : Peak Available Output Current


Figure 4 : Short Circuit Current


Figure 5 : Ripple Rejection


Figure 6 : Dropout Voltage


Figure 7 : Line Transient Response


Figure 8 : Output Voltage


Figure 9 : Quiescent Current


Figure 10 : Load Transient Response


## TYPICAL APPLICATION

BASIC 3A REGULATOR


[^1]TRIMING OUTPUT TO 5V


10A REGULATOR WITH COMPLETE OVERLOAD PROTECTION


* Selected for 20 mA current from unregulated negative supply.
** Solid tantalum.
$A=L M 101 A, L M 201 A$, LM301A.

ADJUSTABLE REGULATOR 0-10V/3A


[^2]TO-3 MECHANICAL DATA

| DIM. | mm. |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A |  | 11.85 |  |  | 0.466 |  |
| B | 0.96 | 1.05 | 1.10 | 0.037 | 0.041 | 0.043 |
| C |  |  | 1.70 |  |  | 0.066 |
| D |  |  | 8.7 |  | 0.429 | 0.342 |
| E |  | 10.9 |  |  |  | 0.665 |
| G |  |  |  |  |  |  |
| P |  |  | 20.0 |  |  | 1.031 |
| R | 3.88 |  | 4.09 | 0.152 |  | 0.161 |
| U |  |  | 39.5 |  |  | 1.555 |
| V |  |  |  |  |  |  |



## TO-220 MECHANICAL DATA

| DIM. | mm. |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A | 4.40 |  | 4.60 | 0.173 |  | 0.181 |
| C | 1.23 |  | 1.32 | 0.048 |  | 0.051 |
| D | 2.40 |  | 2.72 | 0.094 |  | 0.107 |
| D1 |  | 1.27 |  |  | 0.050 |  |
| E | 0.49 |  | 0.70 | 0.019 |  | 0.027 |
| F | 0.61 |  | 0.88 | 0.024 |  | 0.034 |
| F1 | 1.14 |  | 1.70 | 0.044 |  | 0.067 |
| F2 | 1.14 |  | 1.70 | 0.044 |  | 0.067 |
| G | 4.95 |  | 5.15 | 0.194 |  | 0.203 |
| G1 | 2.4 |  | 2.7 | 0.094 |  | 0.106 |
| H2 | 10.0 |  | 10.40 | 0.393 |  | 0.409 |
| L2 |  | 16.4 |  |  | 0.645 |  |
| L4 | 13.0 |  | 14.0 | 0.511 |  | 0.551 |
| L5 | 2.65 |  | 2.95 | 0.104 |  | 0.116 |
| L6 | 15.25 |  | 15.75 | 0.600 |  | 0.620 |
| L7 | 6.2 |  | 6.6 | 0.244 |  | 0.260 |
| L9 | 3.5 |  | 3.93 | 0.137 |  | 0.154 |
| DIA. | 3.75 |  | 3.85 | 0.147 |  | 0.151 |



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## General Description

The LM138 series of adjustable 3-terminal positive voltage regulators is capable of supplying in excess of 5A over a 1.2 V to 32 V output range. They are exceptionally easy to use and require only 2 resistors to set the output voltage. Careful circuit design has resulted in outstanding load and line regulation-comparable to many commercial power supplies. The LM138 family is supplied in a standard 3-lead transistor package.
A unique feature of the LM138 family is time-dependent current limiting. The current limit circuitry allows peak currents of up to 12A to be drawn from the regulator for short periods of time. This allows the LM138 to be used with heavy transient loads and speeds start-up under full-load conditions. Under sustained loading conditions, the current limit decreases to a safe value protecting the regulator. Also included on the chip are thermal overload protection and safe area protection for the power transistor. Overload protection remains functional even if the adjustment pin is accidentally disconnected.
Normally, no capacitors are needed unless the device is situated more than 6 inches from the input filter capacitors in which case an input bypass is needed. An output capacitor can be added to improve transient response, while bypassing the adjustment pin will increase the regulator's ripple rejection.

Besides replacing fixed regulators or discrete designs, the LM138 is useful in a wide variety of other applications. Since the regulator is "floating" and sees only the input-to-output differential voltage, supplies of several hundred volts can be regulated as long as the maximum input to output differential is not exceeded, i.e., do not short-circuit output to ground. The part numbers in the LM138 series which have a K suffix are packaged in a standard Steel TO-3 package, while those with a T suffix are packaged in a TO-220 plastic package. The LM138 is rated for $-55^{\circ} \mathrm{C} \leq \mathrm{T}_{j} \leq+150^{\circ} \mathrm{C}$, and the LM338 is rated for $0^{\circ} \mathrm{C} \leq \mathrm{T}_{J} \leq+125^{\circ} \mathrm{C}$.

## Features

- Guaranteed 7A peak output current
- Guaranteed 5A output current
- Adjustable output down to 1.2 V
- Guaranteed thermal regulation
- Current limit constant with temperature
- ${ }^{+}$Product Enhancement tested
- Output is short-circuit protected


## Applications

- Adjustable power supplies
- Constant current regulators
- Battery chargers

Connection Diagrams (See Physical Dimension section for further information)


Bottom View
Order Number LM138K STEEL or LM338K STEEL See NS Package Number K02A

Absolute Maximum Ratings (Note 1)
If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.
(Note 4)
Power Dissipation
Input/Output Voltage Differential
Storage Temperature

Lead Temperature
$\begin{array}{ll}\text { Metal Package (Soldering, } 10 \text { seconds) } & 300^{\circ} \mathrm{C} \\ \text { Plastic Package (Soldering, } 4 \text { seconds) } & 260^{\circ} \mathrm{C}\end{array}$
TBD

## Operating Temperature Range

| LM138 | $-55^{\circ} \mathrm{C} \leq \mathrm{T}_{J} \leq+150^{\circ} \mathrm{C}$ |
| :--- | ---: |
| LM338 | $0^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{J}} \leq+125^{\circ} \mathrm{C}$ |

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range. Unless otherwise specified, $\mathrm{V}_{\mathrm{IN}}-\mathrm{V}_{\text {OUT }}=5 \mathrm{~V}$; and $\mathrm{I}_{\text {OUT }}=10 \mathrm{~mA}$. (Note 2)

| Symbol | Parameter | Conditions | LM138 |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Typ | Max |  |
| $\mathrm{V}_{\text {REF }}$ | Reference Voltage | $\begin{aligned} & 3 \mathrm{~V} \leq\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }} \leq 35 \mathrm{~V},\right. \\ & 10 \mathrm{~mA} \leq \mathrm{I}_{\text {OUT }} \leq 5 \mathrm{~A}, \mathrm{P} \leq 50 \mathrm{~W} \end{aligned}$ | 1.19 | 1.24 | 1.29 | V |
| $\mathrm{V}_{\text {RLINE }}$ | Line Regulation | $3 \mathrm{~V} \leq\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}\right) \leq 35 \mathrm{~V}$ (Note 3) |  | 0.005 | 0.01 | \%/V |
|  |  |  |  | 0.02 | 0.04 | \%/V |
| $\mathrm{V}_{\text {RLOAD }}$ | Load Regulation | $10 \mathrm{~mA} \leq \mathrm{I}_{\text {OUT }} \leq 5 \mathrm{~A}(\text { Note } 3)$ |  | 0.1 | 0.3 | \% |
|  |  |  |  | 0.3 | 0.6 | \% |
|  | Thermal Regulation | 20 ms Pulse |  | 0.002 | 0.01 | \%/W |
| $\mathrm{I}_{\text {ADJ }}$ | Adjustment Pin Current |  |  | 45 | 100 | $\mu \mathrm{A}$ |
| $\Delta \mathrm{l}_{\text {ADJ }}$ | Adjustment Pin Current Change | $\begin{aligned} & 10 \mathrm{~mA} \leq \mathrm{I}_{\text {OUT }} \leq 5 \mathrm{~A}, \\ & 3 \mathrm{~V} \leq\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}\right) \leq 35 \mathrm{~V} \end{aligned}$ |  | 0.2 | 5 | $\mu \mathrm{A}$ |
| $\Delta \mathrm{V}_{\mathrm{R} / \mathrm{T}}$ | Temperature Stability | $\mathrm{T}_{\text {MIN }} \leq \mathrm{T}_{\mathrm{J}} \leq \mathrm{T}_{\text {MAX }}$ |  | 1 |  | \% |
| $\mathrm{I}_{\text {LOAD }}(\mathrm{Min})$ | Minimum Load Current | $\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}=35 \mathrm{~V}$ |  | 3.5 | 5 | mA |
| $\mathrm{I}_{\mathrm{CL}}$ | Current Limit | $\begin{aligned} & \mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }} \leq 10 \mathrm{~V} \\ & \mathrm{DC} \\ & 0.5 \mathrm{~ms} \text { Peak } \end{aligned}$ | $\begin{array}{r} 5 \\ 7 \\ \hline \end{array}$ | $\begin{gathered} 8 \\ 12 \\ \hline \end{gathered}$ |  | A |
|  |  | $\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}=30 \mathrm{~V}$ |  | 1 | 1 | A |
| $\mathrm{V}_{\mathrm{N}}$ | RMS Output Noise, \% of $\mathrm{V}_{\text {OUT }}$ | $10 \mathrm{~Hz} \leq \mathrm{f} \leq 10 \mathrm{kHz}$ |  | 0.003 |  | \% |
| $\frac{\Delta \mathrm{V}_{\mathrm{R}}}{\Delta \mathrm{~V}_{\mathrm{IN}}}$ | Ripple Rejection Ratio | $\begin{aligned} & \mathrm{V}_{\text {OUT }}=10 \mathrm{~V}, \mathrm{f}=120 \mathrm{~Hz}, \mathrm{C}_{\text {ADJ }}=0 \mu \mathrm{~F} \\ & \mathrm{~V}_{\text {OUT }}=10 \mathrm{~V}, \mathrm{f}=120 \mathrm{~Hz}, \mathrm{C}_{\text {ADJ }}=10 \mu \mathrm{~F} \end{aligned}$ | 60 | $\begin{aligned} & 60 \\ & 75 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
|  | Long-Term Stability | $\mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}, 1000 \mathrm{Hrs}$ |  | 0.3 | 1 | \% |
| $\theta_{\mathrm{JC}}$ | Thermal Resistance, Junction to Case | K Package |  |  | 1 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\theta_{\mathrm{JA}}$ | Thermal Resistance, Junction to Ambient (No Heat Sink) | K Package |  | 35 |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

## Electrical Characteristics

| Symbol | Parameter | Conditions | LM338 |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Typ | Max |  |
| $\mathrm{V}_{\text {REF }}$ | Reference Voltage | $\begin{aligned} & 3 \mathrm{~V} \leq\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }} \leq 35 \mathrm{~V}\right. \\ & 10 \mathrm{~mA} \leq \mathrm{I}_{\text {OUT }} \leq 5 \mathrm{~A}, \mathrm{P} \leq 50 \mathrm{~W} \end{aligned}$ | 1.19 | 1.24 | 1.29 | V |
| $\mathrm{V}_{\text {RLINE }}$ | Line Regulation | $3 \mathrm{~V} \leq\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}\right) \leq 35 \mathrm{~V}$ (Note 3) |  | 0.005 | 0.03 | \%/V |
|  |  |  |  | 0.02 | 0.06 | \%/V |
| $\mathrm{V}_{\text {RLOAD }}$ | Load Regulation | $10 \mathrm{~mA} \leq \mathrm{I}_{\text {OUT }} \leq 5 \mathrm{~A}$ (Note 3) |  | 0.1 | 0.5 | \% |
|  |  |  |  | 0.3 | 1 | \% |
|  | Thermal Regulation | 20 ms Pulse |  | 0.002 | 0.02 | \%/W |
| $\mathrm{I}_{\text {ADJ }}$ | Adjustment Pin Current |  |  | 45 | 100 | $\mu \mathrm{A}$ |
| $\Delta_{\text {ADJ }}$ | Adjustment Pin Current Change | $\begin{aligned} & 10 \mathrm{~mA} \leq \mathrm{I}_{\text {OUT }} \leq 5 \mathrm{~A}, \\ & 3 \mathrm{~V} \leq\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}\right) \leq 35 \mathrm{~V} \end{aligned}$ |  | 0.2 | 5 | $\mu \mathrm{A}$ |

Electrical Characteristics (Continued)

| Symbol | Parameter | Conditions | LM338 |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Typ | Max |  |
| $\Delta \mathrm{V}_{\mathrm{R} / \mathrm{T}}$ | Temperature Stability | $\mathrm{T}_{\text {MIN }} \leq \mathrm{T}_{\mathrm{J}} \leq \mathrm{T}_{\text {MAX }}$ |  | 1 |  | \% |
| $\mathrm{I}_{\text {LOAD }}(\mathrm{Min})$ | Minimum Load Current | $\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}=35 \mathrm{~V}$ |  | 3.5 | 10 | mA |
| $\mathrm{I}_{\mathrm{CL}}$ | Current Limit | $\begin{aligned} & \mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }} \leq 10 \mathrm{~V} \\ & \mathrm{DC} \\ & 0.5 \mathrm{~ms} \text { Peak } \end{aligned}$ | $\begin{aligned} & 5 \\ & 7 \end{aligned}$ | $\begin{gathered} 8 \\ 12 \end{gathered}$ |  | $\begin{aligned} & \text { A } \\ & \text { A } \end{aligned}$ |
|  |  | $\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}=30 \mathrm{~V}$ |  |  | 1 | A |
| $\mathrm{V}_{\mathrm{N}}$ | RMS Output Noise, \% of $\mathrm{V}_{\text {Out }}$ | $10 \mathrm{~Hz} \leq \mathrm{f} \leq 10 \mathrm{kHz}$ |  | 0.003 |  | \% |
| $\frac{\Delta \mathrm{V}_{\mathrm{R}}}{\Delta \mathrm{~V}_{\mathrm{IN}}}$ | Ripple Rejection Ratio | $\begin{aligned} & \mathrm{V}_{\text {OUT }}=10 \mathrm{~V}, \mathrm{f}=120 \mathrm{~Hz}, \mathrm{C}_{\text {ADJ }}=0 \mu \mathrm{~F} \\ & \mathrm{~V}_{\text {OUT }}=10 \mathrm{~V}, \mathrm{f}=120 \mathrm{~Hz}, \mathrm{C}_{\text {ADJ }}=10 \mu \mathrm{~F} \end{aligned}$ | 60 | $\begin{aligned} & 60 \\ & 75 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
|  | Long-Term Stability | $\mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}, 1000 \mathrm{hrs}$ |  | 0.3 | 1 | \% |
| $\theta_{\text {Jc }}$ | Thermal Resistance Junction to Case | K Package <br> T Package |  |  | $\begin{aligned} & 1 \\ & 4 \end{aligned}$ | $\begin{aligned} & { }^{\circ} \mathrm{C} / \mathrm{W} \\ & { }^{\circ} \mathrm{C} / \mathrm{W} \end{aligned}$ |
| $\theta_{J A}$ | Thermal Resistance, Junction to Ambient (No Heat Sink) | K Package <br> T Package |  | $\begin{aligned} & 35 \\ & 50 \end{aligned}$ |  | $\begin{aligned} & { }^{\circ} \mathrm{C} / \mathrm{W} \\ & { }^{\circ} \mathrm{C} / \mathrm{W} \end{aligned}$ |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.
Note 2: These specifications are applicable for power dissipations up to 50 W for the TO-3 (K) package and 25W for the TO-220 (T) package. Power dissipation is guaranteed at these values up to 15 V input-output differential. Above 15 V differential, power dissipation will be limited by internal protection circuitry. All limits (i.e., the numbers in the Min. and Max. columns) are guaranteed to National's AOQL (Average Outgoing Quality Level).
Note 3: Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specifications for thermal regulation.
Note 4: Refer to RETS138K drawing for military specifications of LM138K.

## Typical Performance Characteristics



DS009060-32

## Load Regulation



Current Limit


Dropout Voltage


Current Limit


Adjustment
Current


## Typical Performance Characteristics (Continued)



Ripple Rejection


## Line Transient Response



## Output Impedance



## Ripple Rejection



Minimum Operating Current


## Ripple Rejection



Load Transient Response


## Application Hints

In operation, the LM138 develops a nominal 1.25 V reference voltage, $\mathrm{V}_{\text {REF }}$, between the output and adjustment terminal. The reference voltage is impressed across program resistor R1 and, since the voltage is constant, a constant current $\mathrm{I}_{1}$ then flows through the output set resistor R2, giving an output voltage of

$$
V_{\text {OUT }}=V_{\text {REF }}\left(1+\frac{R 2}{R 1}\right)+I_{A D J} R 2 .
$$

## Application Hints (Continued)



## FIGURE 1.

Since the $50 \mu \mathrm{~A}$ current from the adjustment terminal represents an error term, the LM138 was designed to minimize $I_{\text {ADJ }}$ and make it very constant with line and load changes. To do this, all quiescent operating current is returned to the output establishing a minimum load current requirement. If there is insufficient load on the output, the output will rise.

## External Capacitors

An input bypass capacitor is recommended. A $0.1 \mu \mathrm{~F}$ disc or $1 \mu \mathrm{~F}$ solid tantalum on the input is suitable input bypassing for almost all applications. The device is more sensitive to the absence of input bypassiing when adjustment or output capacitors are used but the above values will eliminate the possiblity of problems.
The adjustment terminal can be bypassed to ground on the LM138 to improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. With a $10 \mu \mathrm{~F}$ bypass capacitor 75 dB ripple rejection is obtainable at any output level. Increases over $20 \mu \mathrm{~F}$ do not appreciably improve the ripple rejection at frequencies above 120 Hz . If the bypass capacitor is used, it is sometimes necessary to include protection diodes to prevent the capacitor from discharging through internal low current paths and damaging the device.
In general, the best type of capacitors to use are solid tantalum. Solid tantalum capacitors have low impedance even at high frequencies. Depending upon capacitor construction, it takes about $25 \mu \mathrm{~F}$ in aluminum electrolytic to equal $1 \mu \mathrm{~F}$ solid tantalum at high frequencies. Ceramic capacitors are also good at high frequencies; but some types have a large decrease in capacitance at frequencies around 0.5 MHz . For this reason, $0.01 \mu \mathrm{~F}$ disc may seem to work better than a $0.1 \mu \mathrm{~F}$ disc as a bypass.
Although the LM138 is stable with no output capacitors, like any feedback circuit, certain values of external capacitance can cause excessive ringing. This occurs with values between 500 pF and 5000 pF . A $1 \mu \mathrm{~F}$ solid tantalum (or $25 \mu \mathrm{~F}$ aluminum electrolytic) on the output swamps this effect and insures stability.

## Load Regulation

The LM138 is capable of providing extremely good load regulation but a few precautions are needed to obtain maximum performance. The current set resistor connected between the adjustment terminal and the output terminal (usually $240 \Omega$ ) should be tied directly to the output of the regulator (case) rather than near the load. This eliminates line drops from appearing effectively in series with the reference and degrading regulation. For example, a 15 V regulator with $0.05 \Omega$ resistance between the regulator and load will have a load regulation due to line resistance of $0.05 \Omega \times \mathrm{I}_{\mathrm{L}}$. If the set resistor is connected near the load the effective line resistance will be $0.05 \Omega(1+\mathrm{R} 2 / \mathrm{R} 1)$ or in this case, 11.5 times worse.
Figure 2 shows the effect of resistance between the regulator and $240 \Omega$ set resistor.


FIGURE 2. Regulator with Line Resistance in Output Lead

With the TO-3 package, it is easy to minimize the resistance from the case to the set resistor, by using 2 separate leads to the case. The ground of R2 can be returned near the ground of the load to provide remote ground sensing and improve load regulation.

## Protection Diodes

When external capacitors are used with any IC regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator. Most $20 \mu \mathrm{~F}$ capacitors have low enough internal series resistance to deliver 20A spikes when shorted. Although the surge is short, there is enough energy to damage parts of the IC.
When an output capacitor is connected to a regulator and the input is shorted, the output capacitor will discharge into the output of the regulator. The discharge current depends on the value of the capacitor, the output voltage of the regulator, and the rate of decrease of $\mathrm{V}_{\mathrm{IN}}$. In the LM138 this discharge path is through a large junction that is able to sustain 25A surge with no problem. This is not true of other types of positive regulators. For output capacitors of $100 \mu \mathrm{~F}$ or less at output of 15 V or less, there is no need to use diodes.
The bypass capacitor on the adjustment terminal can discharge through a low current junction. Discharge occurs when either the input or output is shorted. Internal to the LM138 is a $50 \Omega$ resistor which limits the peak discharge current. No protection is needed for output voltages of 25 V or less and $10 \mu \mathrm{~F}$ capacitance. Figure 3 shows an LM138 with protection diodes included for use with outputs greater than 25 V and high values of output capacitance.


## Typical Applications



Full output current not available
at high input-output voltages
tOptional - improves transient response. Output capacitors in the range o
$1 \mu \mathrm{~F}$ to $1000 \mu \mathrm{~F}$ of aluminum or tantalum electrolytic are commonly used
to provide improved output impedance and rejection of transients.
*Needed if device is more than 6 inches from filter capacitors.
$\dagger \dagger V_{\text {OUT }}=1.25 \mathrm{~V}\left(1+\frac{\mathrm{R}_{2}}{\mathrm{R} 1}\right)+\mathrm{I}_{\text {ADJ }}\left(\mathrm{R}_{2}\right)$
${ }^{* * R} 1=240 \Omega$ for LM138. R1, R2 as an assembly can be ordered from Bourns:
MIL part no. 7105A-AT2-502
COMM part no. 7105A-AT7-502


## Typical Applications


Slow Turn-On 15V Regulator


Adjustable Regulator with Improved Ripple Rejection

†Solid tantalum
*Discharges C1 if output is shorted to ground
**R1 = 240 $\Omega$ for LM138

*Sets maximum $\mathrm{V}_{\text {OUT }}$
**R1 = $240 \Omega$ for LM138


Typical Applications (Continued)


## Typical Applications (Continued)



Tracking Preregulator

† Minimum load - 10 mA

* All outputs within $\pm 100 \mathrm{mV}$


## Typical Applications (Continued)



* $\mathrm{R}_{\mathrm{S}}$ —sets output impedance of charger $\mathrm{Z}_{\mathrm{OUT}}=\mathrm{R}_{\mathrm{S}}\left(1+\frac{\mathrm{R} 2}{\mathrm{R} 1}\right)$

Use of $R_{S}$ allows low charging rates with fully charged battery.
**The $1000 \mu \mathrm{~F}$ is recommended to filter out input transients

Typical Applications (Continued)


Minimum load - 100 mA

Physical Dimensions inches (millimeters) unless otherwise noted

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## LM124/LM224/LM324/LM2902 Low Power Quad Operational Amplifiers

## General Description

The LM124 series consists of four independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.
Application areas include transducer amplifiers, DC gain blocks and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the LM124 series can be directly operated off of the standard +5 V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional $\pm 15 \mathrm{~V}$ power supplies.

## Unique Characteristics

- In the linear mode the input common-mode voltage range includes ground and the output voltage can also swing to ground, even though operated from only a single power supply voltage
- The unity gain cross frequency is temperature compensated
- The input bias current is also temperature compensated


## Advantages

- Eliminates need for dual supplies
- Four internally compensated op amps in a single package
- Allows directly sensing near GND and $\mathrm{V}_{\text {OUT }}$ also goes to GND
- Compatible with all forms of logic
- Power drain suitable for battery operation


## Features

- Internally frequency compensated for unity gain
- Large DC voltage gain 100 dB
- Wide bandwidth (unity gain) 1 MHz (temperature compensated)
- Wide power supply range:

Single supply 3 V to 32 V
or dual supplies $\pm 1.5 \mathrm{~V}$ to $\pm 16 \mathrm{~V}$

- Very low supply current drain ( $700 \mu \mathrm{~A}$ ) - essentially independent of supply voltage
- Low input biasing current 45 nA (temperature compensated)
- Low input offset voltage 2 mV and offset current: 5 nA
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Large output voltage swing 0 V to $\mathrm{V}^{+}-1.5 \mathrm{~V}$


Order Number LM124AW/883, LM124AWG/883, LM124W/883 or LM124WG/883 LM124AWRQML and LM124AWRQMLV(Note 3)

See NS Package Number W14B
LM124AWGRQML and LM124AWGRQMLV(Note 3) See NS Package Number WG14A

Note 1: LM124A available per JM38510/11006
Note 2: LM124 available per JM38510/11005
Note 3: See STD Mil DWG 5962R99504 for Radiation Tolerant Device

## Schematic Diagram (Each Amplifier)



Absolute Maximum Ratings (Note 12)
If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/

|  | LM124/LM224/LM324 LM124A/LM224A/LM324A | LM2902 |
| :---: | :---: | :---: |
| Supply Voltage, $\mathrm{V}^{+}$ | 32 V | 26V |
| Differential Input Voltage | 32 V | 26 V |
| Input Voltage | -0.3 V to +32 V | -0.3 V to +26 V |
| Input Current |  |  |
| $\left(\mathrm{V}_{\mathrm{IN}}<-0.3 \mathrm{~V}\right)$ (Note 6) | 50 mA | 50 mA |
| Power Dissipation (Note 4) |  |  |
| Molded DIP | 1130 mW | 1130 mW |
| Cavity DIP | 1260 mW | 1260 mW |
| Small Outline Package | 800 mW | 800 mW |
| Output Short-Circuit to GND (One Amplifier) (Note 5) |  |  |
|  |  |  |
| $\mathrm{V}^{+} \leq 15 \mathrm{~V}$ and $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | Continuous | Continuous |
| Operating Temperature Range |  | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| LM324/LM324A | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |  |
| LM224/LM224A | $-25^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  |
| LM124/LM124A | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |  |
| Storage Temperature Range | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Lead Temperature (Soldering, 10 seconds) | $260^{\circ} \mathrm{C}$ | $260^{\circ} \mathrm{C}$ |
| Soldering Information |  |  |
| Dual-In-Line Package |  |  |
| Soldering (10 seconds) | $260{ }^{\circ} \mathrm{C}$ | $260{ }^{\circ} \mathrm{C}$ |
| Small Outline Package |  |  |
| Vapor Phase (60 seconds) | $215^{\circ} \mathrm{C}$ | $215{ }^{\circ} \mathrm{C}$ |
| Infrared (15 seconds) | $220^{\circ} \mathrm{C}$ | $220^{\circ} \mathrm{C}$ |

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.
ESD Tolerance (Note 13)

250 V
250 V

## Electrical Characteristics

$\mathrm{V}^{+}=+5.0 \mathrm{~V}$, (Note 7 ), unless otherwise stated

| Parameter | Conditions | LM124A |  |  | LM224A |  |  | LM324A |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max |  |
| Input Offset Voltage | (Note 8) $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  | 1 | 2 |  | 1 | 3 |  | 2 | 3 | mV |
| Input Bias Current (Note 9) | $\begin{aligned} & \mathrm{I}_{\mathrm{IN(+)}} \text { or } \mathrm{I}_{\mathrm{IN(-)}}, \mathrm{~V}_{\mathrm{CM}}=0 \mathrm{~V}, \\ & \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  | 20 | 50 |  | 40 | 80 |  | 45 | 100 | nA |
| Input Offset Current | $\begin{aligned} & \mathrm{I}_{\mathrm{IN}(+)} \text { or } \mathrm{I}_{\mathrm{IN(-)},}, \mathrm{~V}_{\mathrm{CM}}=0 \mathrm{~V}, \\ & \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  | 2 | 10 |  | 2 | 15 |  | 5 | 30 | nA |
| Input Common-Mode Voltage Range (Note 10) | $\begin{aligned} & \mathrm{V}^{+}=30 \mathrm{~V},\left(\mathrm{LM} 2902, \mathrm{~V}^{+}=26 \mathrm{~V}\right), \\ & \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ | 0 |  | $\mathrm{V}^{+}-1.5$ | 0 |  | $\mathrm{V}^{+}-1.5$ | 0 |  | $\mathrm{V}^{+}-1.5$ | V |
| Supply Current | Over Full Temperature Range $\begin{aligned} & \mathrm{R}_{\mathrm{L}}=\infty \text { On All Op Amps } \\ & \mathrm{V}^{+}=30 \mathrm{~V}\left(\mathrm{LM} 2902 \mathrm{~V}^{+}=26 \mathrm{~V}\right) \\ & \mathrm{V}^{+}=5 \mathrm{~V} \end{aligned}$ |  | $\begin{aligned} & 1.5 \\ & 0.7 \end{aligned}$ | $\begin{gathered} 3 \\ 1.2 \end{gathered}$ |  | $\begin{aligned} & 1.5 \\ & 0.7 \end{aligned}$ | $\begin{gathered} 3 \\ 1.2 \end{gathered}$ |  |  | $\begin{gathered} 3 \\ 1.2 \end{gathered}$ | mA |
| Large Signal Voltage Gain | $\begin{aligned} & \mathrm{V}^{+}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{L}} \geq 2 \mathrm{k} \Omega, \\ & \left(\mathrm{~V}_{\mathrm{O}}=1 \mathrm{~V} \text { to } 11 \mathrm{~V}\right), \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ | 50 | 100 |  | 50 | 100 |  | 25 | 100 |  | V/mV |
| Common-Mode | $\mathrm{DC}, \mathrm{V}_{\mathrm{CM}}=0 \mathrm{~V}$ to $\mathrm{V}^{+}-1.5 \mathrm{~V}$, | 70 | 85 |  | 70 | 85 |  | 65 | 85 |  | dB |

Electrical Characteristics
(Continued)
$\mathrm{V}^{+}=+5.0 \mathrm{~V}$, (Note 7 ), unless otherwise stated

| Parameter |  | Conditions |  | LM124A |  |  | LM224A |  |  | LM324A |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max |  |
| Rejection Ratio |  |  |  | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  |  |  |  |  |  |  |  |  |  |
| Power Supply Rejection Ratio |  | $\begin{aligned} & \mathrm{V}^{+}=5 \mathrm{~V} \text { to } 30 \mathrm{~V} \\ & \left(\mathrm{LM} 2902, \mathrm{~V}^{+}=5 \mathrm{~V} \text { to } 26 \mathrm{~V}\right), \\ & \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  |  | 100 |  | 65 | 100 |  | 65 | 100 |  | dB |
| Amplifier-to-Amplifier Coupling (Note 11) |  | $\begin{aligned} & \mathrm{f}=1 \mathrm{kHz} \text { to } 20 \mathrm{kHz}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \\ & \text { (Input Referred) } \end{aligned}$ |  |  | -120 |  |  | -120 |  |  | -120 |  | dB |
| Output <br> Current | Source | $\mathrm{V}_{\mathrm{IN}}{ }^{+}=1 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}^{-}=0 \mathrm{~V},$$\mathrm{V}^{+}=15 \mathrm{~V}, \mathrm{~V}_{\mathrm{O}}=2 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | 40 |  | 20 | 40 |  | 20 | 40 |  | mA |
|  | Sink | $\begin{array}{\|l} \hline \mathrm{V}_{\mathrm{IN}^{-}}=1 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}^{+}}=0 \mathrm{~V}, \\ \mathrm{~V}^{+}=15 \mathrm{~V}, \mathrm{~V}_{\mathrm{O}}=2 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \\ \hline \end{array}$ |  |  | 20 |  | 10 | 20 |  | 10 | 20 |  |  |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}^{-}}=1 \mathrm{~V}, \mathrm{~V}_{\text {IN }}^{+}=0 \mathrm{~V}, \\ & \mathrm{~V}^{+}=15 \mathrm{~V}, \mathrm{~V}_{\mathrm{O}}=200 \mathrm{mV}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  |  | 50 |  | 12 | 50 |  | 12 | 50 |  | $\mu \mathrm{A}$ |
| Short Circuit to Ground |  | (Note 5) $\mathrm{V}^{+}=15 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | 40 | 60 |  | 40 | 60 |  | 40 | 60 | mA |
| Input Offset Voltage |  | (Note 8) |  |  |  | 4 |  |  | 4 |  |  | 5 | mV |
| $\mathrm{V}_{\text {Os }}$ Drift |  | $\mathrm{R}_{\mathrm{S}}=0 \Omega$ |  |  | 7 | 20 |  | 7 | 20 |  | 7 | 30 | $\mu \mathrm{V} /{ }^{\circ} \mathrm{C}$ |
| Input Offset Current |  | $\mathrm{I}_{\mathrm{IN}(+)}-\mathrm{I}_{\mathrm{IN}(-)}, \mathrm{V}_{\mathrm{CM}}=0 \mathrm{~V}$ |  |  |  | 30 |  |  | 30 |  |  | 75 | nA |
| Ios Drift |  | $\mathrm{R}_{\mathrm{S}}=0 \Omega$ |  |  | 10 | 200 |  | 10 | 200 |  | 10 | 300 | $\mathrm{pA} /{ }^{\circ} \mathrm{C}$ |
| Input Bias Current |  | $\mathrm{I}_{\operatorname{IN}(+)}$ or $\mathrm{I}_{\operatorname{IN}(-)}$ |  |  | 40 | 100 |  | 40 | 100 |  | 40 | 200 | nA |
| Input Common-Mode Voltage Range (Note 10) |  | $\begin{aligned} & \mathrm{V}^{+}=+30 \mathrm{~V} \\ & \left(\mathrm{LM} 2902, \mathrm{~V}^{+}=26 \mathrm{~V}\right) \\ & \\ & \mathrm{V}^{+}=+15 \mathrm{~V}\left(\mathrm{~V}_{\mathrm{O}} \text { Swing }=1 \mathrm{~V} \text { to } 11 \mathrm{~V}\right) \\ & \mathrm{R}_{\mathrm{L}} \geq 2 \mathrm{k} \Omega \end{aligned}$ |  | 0 |  | $\mathrm{V}^{+}-2$ | 0 |  | $\mathrm{V}^{+}-2$ | 0 |  | $\mathrm{V}^{+}-2$ | V |
| Large Signal Voltage Gain |  |  |  | 25 |  |  | 25 |  |  | 15 |  |  | V/mV |
| Output <br> Voltage <br> Swing | $\mathrm{V}_{\mathrm{OH}}$ | $\begin{aligned} & \mathrm{V}^{+}=30 \mathrm{~V} \\ & \left(\mathrm{LM} 2902, \mathrm{~V}^{+}=26 \mathrm{~V}\right) \end{aligned}$ | $\mathrm{R}_{\mathrm{L}}=2 \mathrm{k} \Omega$ | 26 |  |  | 26 |  |  | 26 |  |  | V |
|  |  |  | $\mathrm{R}_{\mathrm{L}}=10 \mathrm{k} \Omega$ |  |  |  | 27 |  |  | 27 | 28 |  |  |
|  | $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{V}^{+}=5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=10 \mathrm{k} \Omega$ |  |  | 5 |  |  | 5 | 20 |  | 5 | 20 | mV |
| Output <br> Current | Source | $\mathrm{V}_{\mathrm{O}}=2 \mathrm{~V} \quad$ <br>  <br>  <br>  <br>  <br>  <br>  <br>  <br>  <br> $\mathrm{IN}^{+}=+1 \mathrm{~V}$, <br> $\mathrm{V}^{+}=0 \mathrm{~V}$, <br>  <br>  <br> $\mathrm{VIN}^{-}=+15 \mathrm{~V}$, <br> $\mathrm{V}_{\mathrm{IN}}^{+}=0 \mathrm{~V}$, <br> $\mathrm{V}^{+}=15 \mathrm{~V}$ |  |  |  |  | 10 | 20 |  | 10 | 20 |  | mA |
|  | Sink |  |  |  |  |  | 5 | 8 |  | 5 | 8 |  |  |

## Electrical Characteristics

$\mathrm{V}^{+}=+5.0 \mathrm{~V}$, (Note 7), unless otherwise stated

| Parameter | Conditions | LM124/LM224 |  |  | LM324 |  |  | LM2902 |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max |  |
| Input Offset Voltage | (Note 8) $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  | 2 | 5 |  | 2 | 7 |  | 2 | 7 | mV |
| Input Bias Current (Note 9) | $\begin{aligned} & \mathrm{I}_{\mathrm{IN}(+)} \text { or } \mathrm{I}_{\mathrm{IN(-)}}, \mathrm{~V}_{\mathrm{CM}}=0 \mathrm{~V}, \\ & \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  | 45 | 150 |  | 45 | 250 |  | 45 | 250 | nA |
| Input Offset Current | $\begin{aligned} & \mathrm{I}_{\mathrm{IN}(+)} \text { or } \mathrm{I}_{\mathrm{IN}(-)}, \mathrm{V}_{\mathrm{CM}}=0 \mathrm{~V}, \\ & \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  | 3 | 30 |  | 5 | 50 |  | 5 | 50 | nA |
| Input Common-Mode <br> Voltage Range (Note 10) | $\begin{aligned} & \mathrm{V}^{+}=30 \mathrm{~V},\left(\mathrm{LM} 2902, \mathrm{~V}^{+}=26 \mathrm{~V}\right), \\ & \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ | 0 |  | $\mathrm{V}^{+}-1.5$ | 0 |  | $\mathrm{V}^{+}-1.5$ | 0 |  | $\mathrm{V}^{+}-1.5$ | V |

Electrical Characteristics
(Continued)
$\mathrm{V}^{+}=+5.0 \mathrm{~V}$, (Note 7 ), unless otherwise stated

| Parameter |  | Conditions |  | LM124/LM224 |  |  | LM324 |  |  | LM2902 |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max |  |
| Supply Current |  |  |  | Over Full Temperature Range$\begin{aligned} & \mathrm{R}_{\mathrm{L}}=\infty \text { On All Op Amps } \\ & \mathrm{V}^{+}=30 \mathrm{~V}\left(\mathrm{LM} 2902 \mathrm{~V}^{+}=26 \mathrm{~V}\right) \\ & \mathrm{V}^{+}=5 \mathrm{~V} \end{aligned}$ |  |  |  | $\begin{gathered} 3 \\ 1.2 \end{gathered}$ |  | $\begin{aligned} & 1.5 \\ & 0.7 \end{aligned}$ | $\begin{gathered} 3 \\ 1.2 \end{gathered}$ |  | $\begin{aligned} & 1.5 \\ & 0.7 \end{aligned}$ | $\begin{gathered} 3 \\ 1.2 \end{gathered}$ | mA |
| Large Signal Voltage Gain |  | $\begin{aligned} & \mathrm{V}^{+}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{L}} \geq 2 \mathrm{k} \Omega, \\ & \left(\mathrm{~V}_{\mathrm{O}}=1 \mathrm{~V} \text { to } 11 \mathrm{~V}\right), \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \\ & \hline \end{aligned}$ |  | 50 | 100 |  | 25 | 100 |  | 25 | 100 |  | V/mV |
| Common-Mode Rejection Ratio |  | $\begin{aligned} & \mathrm{DC}, \mathrm{~V}_{\mathrm{CM}}=0 \mathrm{~V} \text { to } \mathrm{V}^{+}-1.5 \mathrm{~V}, \\ & \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  | 70 | 85 |  | 65 | 85 |  | 50 | 70 |  | dB |
| Power Supply Rejection Ratio |  | $\begin{aligned} & \mathrm{V}^{+}=5 \mathrm{~V} \text { to } 30 \mathrm{~V} \\ & \left(\mathrm{LM} 2902, \mathrm{~V}^{+}=5 \mathrm{~V} \text { to } 26 \mathrm{~V}\right), \\ & \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \\ & \hline \end{aligned}$ |  |  |  |  | 65 | 100 |  | 50 | 100 |  | dB |
| Amplifier-to-Amplifier Coupling (Note 11) |  | $\begin{aligned} & \mathrm{f}=1 \mathrm{kHz} \text { to } 20 \mathrm{kHz}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \\ & \text { (Input Referred) } \end{aligned}$ |  |  | -120 |  |  | -120 |  |  | -120 |  | dB |
| Output <br> Current | Source | $\mathrm{V}_{\mathrm{IN}}^{+}=1 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}^{-}}=0 \mathrm{~V},$$\mathrm{V}^{+}=15 \mathrm{~V}, \mathrm{~V}_{\mathrm{O}}=2 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  | 20 | 40 |  | 20 | 40 |  | 20 | 40 |  | mA |
|  | Sink | $\begin{aligned} & \mathrm{V}_{\text {IN }^{-}=1 \mathrm{~V},}, \mathrm{~V}_{\mathrm{IN}^{+}}=0 \mathrm{~V}, \\ & \mathrm{~V}^{+}=15 \mathrm{~V}, \mathrm{~V}_{\mathrm{O}}=2 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  | 10 | 20 |  | 10 | 20 |  | 10 | 20 |  |  |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}^{-}}=1 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}^{+}}=0 \mathrm{~V}, \\ & \mathrm{~V}^{+}=15 \mathrm{~V}, \mathrm{~V}_{\mathrm{O}}=200 \mathrm{mV}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \end{aligned}$ |  | 12 | 50 |  | 12 | 50 |  | 12 | 50 |  | $\mu \mathrm{A}$ |
| Short Circuit to Ground |  | (Note 5) $\mathrm{V}^{+}=15 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  |  | 60 |  | 40 | 60 |  | 40 | 60 | mA |
| Input Offset Voltage |  | (Note 8) |  |  |  | 7 |  |  | 9 |  |  | 10 | mV |
| $\mathrm{V}_{\text {Os }}$ Drift |  | $\mathrm{R}_{\mathrm{S}}=0 \Omega$ |  |  | 7 |  |  | 7 |  |  | 7 |  | $\mu \mathrm{V} /{ }^{\circ} \mathrm{C}$ |
| Input Offset Current |  | $\mathrm{I}_{\mathrm{IN}(+)}-\mathrm{I}_{\mathrm{IN}(-)}, \mathrm{V}_{\mathrm{CM}}=0 \mathrm{~V}$ |  |  |  | 100 |  |  | 150 |  | 45 | 200 | nA |
| Ios Drift |  | $\mathrm{R}_{\mathrm{S}}=0 \Omega$ |  |  | 10 |  |  | 10 |  |  | 10 |  | $\mathrm{pA} /{ }^{\circ} \mathrm{C}$ |
| Input Bias Current |  | $\mathrm{I}_{\operatorname{IN}(+)}$ or $\mathrm{I}_{\operatorname{IN}(-)}$ |  |  | 40 | 300 |  | 40 | 500 |  | 40 | 500 | nA |
| Input Common-Mode Voltage Range (Note 10) |  | $\begin{aligned} & \mathrm{V}^{+}=+30 \mathrm{~V} \\ & \left(\mathrm{LM} 2902, \mathrm{~V}^{+}=26 \mathrm{~V}\right) \end{aligned}$ |  | 0 |  | $\mathrm{V}^{+}-2$ | 0 |  | $\mathrm{V}^{+}-2$ | 0 |  | $\mathrm{V}^{+}-2$ | V |
| Large Signal Voltage Gain |  | $\begin{aligned} & \mathrm{V}^{+}=+15 \mathrm{~V}\left(\mathrm{~V}_{\mathrm{O}} \text { Swing = } 1 \mathrm{~V} \text { to } 11 \mathrm{~V}\right) \\ & \mathrm{R}_{\mathrm{L}} \geq 2 \mathrm{k} \Omega \end{aligned}$ |  | 25 |  |  | 15 |  |  | 15 |  |  | V/mV |
| Output <br> Voltage <br> Swing | $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{V}^{+}=30 \mathrm{~V}$ $\text { (LM2902, } \left.\mathrm{V}^{+}=26 \mathrm{~V}\right)$ | $\mathrm{R}_{\mathrm{L}}=2 \mathrm{k} \Omega$ | 26 |  |  | 26 |  |  | 22 |  |  | V |
|  | $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{V}^{+}=5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=10 \mathrm{k} \Omega$ |  | 27 | 28 5 | 20 | 27 | 28 5 | 20 | 23 | 24 5 | 100 | mV |
| Output <br> Current | Source | $\mathrm{V}_{\mathrm{O}}=2 \mathrm{~V}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}^{+}}=+1 \mathrm{~V}, \\ & \mathrm{~V}_{\mathrm{IN}^{-}}=0 \mathrm{~V}, \\ & \mathrm{~V}^{+}=15 \mathrm{~V} \end{aligned}$ | 10 | 20 |  | 10 | 20 |  | 10 | 20 |  | mA |
|  | Sink |  | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}^{-}}=+1 \mathrm{~V}, \\ & \mathrm{~V}_{\mathrm{IN}^{+}}=0 \mathrm{~V}, \\ & \mathrm{~V}^{+}=15 \mathrm{~V} \end{aligned}$ | 5 | 8 |  | 5 | 8 |  | 5 | 8 |  |  |

Note 4: For operating at high temperatures, the LM324/LM324A/LM2902 must be derated based on a $+125^{\circ} \mathrm{C}$ maximum junction temperature and a thermal resistance of $88^{\circ} \mathrm{C} / \mathrm{W}$ which applies for the device soldered in a printed circuit board, operating in a still air ambient. The LM224/LM224A and LM124/LM124A can be derated based on $\mathrm{a}+150^{\circ} \mathrm{C}$ maximum junction temperature. The dissipation is the total of all four amplifiers - use external resistors, where possible, to allow the amplifier to saturate of to reduce the power which is dissipated in the integrated circuit.
Note 5: Short circuits from the output to $\mathrm{V}^{+}$can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 40 mA independent of the magnitude of $\mathrm{V}^{+}$. At values of supply voltage in excess of +15 V , continuous short-circuits can exceed the power dissipation ratings and cause eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.
Note 6: This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action

## Electrical Characteristics <br> (Continued)

on the IC chip. This transistor action can cause the output voltages of the op amps to go to the $\mathrm{V}^{+}$voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3 V (at $25^{\circ} \mathrm{C}$ ).
Note 7: These specifications are limited to $-55^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq+125^{\circ} \mathrm{C}$ for the LM124/LM124A. With the LM224/LM224A, all temperature specifications are limited to $-25^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq+85^{\circ} \mathrm{C}$, the $\mathrm{LM} 324 / \mathrm{LM} 324 \mathrm{~A}$ temperature specifications are limited to $0^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq+70^{\circ} \mathrm{C}$, and the LM 2902 specifications are limited to $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq$ $+85^{\circ} \mathrm{C}$.
Note 8: $\mathrm{V}_{\mathrm{O}} \simeq 1.4 \mathrm{~V}, \mathrm{R}_{\mathrm{S}}=0 \Omega$ with $\mathrm{V}^{+}$from 5 V to 30 V ; and over the full input common-mode range ( 0 V to $\mathrm{V}^{+}-1.5 \mathrm{~V}$ ) for $\mathrm{LM} 2902, \mathrm{~V}^{+}$from 5 V to 26 V .
Note 9: The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.
Note 10: The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3 V (at $25^{\circ} \mathrm{C}$ ). The upper end of the common-mode voltage range is $\mathrm{V}^{+}-1.5 \mathrm{~V}$ (at $25^{\circ} \mathrm{C}$ ), but either or both inputs can go to +32 V without damage ( +26 V for LM2902), independent of the magnitude of $\mathrm{V}^{+}$.

Note 11: Due to proximity of external components, insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequencies.

Note 12: Refer to RETS124AX for LM124A military specifications and refer to RETS124X for LM124 military specifications.
Note 13: Human body model, $1.5 \mathrm{k} \Omega$ in series with 100 pF

## Typical Performance Characteristics



Typical Performance Characteristics (Continued)



## Application Hints

The LM124 series are op amps which operate with only a single power supply voltage, have true-differential inputs, and remain in the linear mode with an input common-mode voltage of $0 \mathrm{~V}_{\mathrm{DC}}$. These amplifiers operate over a wide range of power supply voltage with little change in performance characteristics. At $25^{\circ} \mathrm{C}$ amplifier operation is possible down to a minimum supply voltage of $2.3 \mathrm{~V}_{\mathrm{DC}}$.
The pinouts of the package have been designed to simplify PC board layouts. Inverting inputs are adjacent to outputs for all of the amplifiers and the outputs have also been placed at the corners of the package (pins 1, 7, 8, and 14).
Precautions should be taken to insure that the power supply for the integrated circuit never becomes reversed in polarity or that the unit is not inadvertently installed backwards in a test socket as an unlimited current surge through the resulting forward diode within the IC could cause fusing of the internal conductors and result in a destroyed unit.
Large differential input voltages can be easily accommodated and, as input differential voltage protection diodes are not needed, no large input currents result from large differential input voltages. The differential input voltage may be larger than $\mathrm{V}^{+}$without damaging the device. Protection
should be provided to prevent the input voltages from going negative more than $-0.3 \mathrm{~V}_{\mathrm{DC}}$ (at $25^{\circ} \mathrm{C}$ ). An input clamp diode with a resistor to the IC input terminal can be used.
To reduce the power supply drain, the amplifiers have a class A output stage for small signal levels which converts to class B in a large signal mode. This allows the amplifiers to both source and sink large output currents. Therefore both NPN and PNP external current boost transistors can be used to extend the power capability of the basic amplifiers. The output voltage needs to raise approximately 1 diode drop above ground to bias the on-chip vertical PNP transistor for output current sinking applications.
For ac applications, where the load is capacitively coupled to the output of the amplifier, a resistor should be used, from the output of the amplifier to ground to increase the class A bias current and prevent crossover distortion.
Where the load is directly coupled, as in dc applications, there is no crossover distortion.
Capacitive loads which are applied directly to the output of the amplifier reduce the loop stability margin. Values of 50 pF can be accommodated using the worst-case noninverting unity gain connection. Large closed loop gains or resistive isolation should be used if larger load capacitance must be driven by the amplifier.

## Application Hints

(Continued)
The bias network of the LM124 establishes a drain current which is independent of the magnitude of the power supply voltage over the range of from $3 \mathrm{~V}_{\mathrm{DC}}$ to $30 \mathrm{~V}_{\mathrm{DC}}$.
Output short circuits either to ground or to the positive power supply should be of short time duration. Units can be destroyed, not as a result of the short circuit current causing metal fusing, but rather due to the large increase in IC chip dissipation which will cause eventual failure due to excessive junction temperatures. Putting direct short-circuits on more than one amplifier at a time will increase the total IC power dissipation to destructive levels, if not properly protected with external dissipation limiting resistors in series with the output leads of the amplifiers. The larger value of
output source current which is available at $25^{\circ} \mathrm{C}$ provides a larger output current capability at elevated temperatures (see typical performance characteristics) than a standard IC op amp.
The circuits presented in the section on typical applications emphasize operation on only a single power supply voltage. If complementary power supplies are available, all of the standard op amp circuits can be used. In general, introducing a pseudo-ground (a bias voltage reference of $\mathrm{V}^{+} / 2$ ) will allow operation above and below this value in single power supply systems. Many application circuits are shown which take advantage of the wide input common-mode voltage range which includes ground. In most cases, input biasing is not required and input voltages which range to ground can easily be accommodated.

## Typical Single-Supply Applications ( $\mathrm{V}^{+}=5.0 \mathrm{~V}_{\mathrm{DC}}$ )



Typical Single-Supply Applications ( $\mathrm{V}^{+}=5.0 \mathrm{~V}_{\mathrm{DC}}$ ) (Continued)


$$
I_{2}=\left(\frac{R 1}{R 2}\right) I_{1}
$$

Typical Single-Supply Applications
$\left(\mathrm{V}+=5.0 \mathrm{~V}_{\mathrm{DC}}\right)$ (Continued)


Typical Single-Supply Applications ( $\mathrm{V}^{+}=5.0 \mathrm{~V}_{\mathrm{DC}}$ ) (Continued)

## High Compliance Current Sink



$$
\begin{aligned}
& \mathrm{I}_{\mathrm{O}}=1 \text { amp/volt } \mathrm{V}_{\mathrm{IN}} \\
& \text { (Increase } \mathrm{R}_{\mathrm{E}} \text { for } \mathrm{I}_{\mathrm{O}} \text { small) }
\end{aligned}
$$



Typical Single-Supply Applications $\left(\mathrm{V}^{+}=5.0 \mathrm{~V} \mathrm{DC}\right)$ (Continued)


Ground Referencing a Differential Input Signal


Photo Voltaic-Cell Amplifier


Typical Single-Supply Applications ( $\mathrm{V}^{+}=5.0 \mathrm{~V}_{\mathrm{DC}}$ ) (Continued)


$$
A_{V}=\frac{R_{f}}{R 1}\left(A s \text { shown, } A_{V}=10\right)
$$

AC Coupled Non-Inverting Amplifier


Typical Single-Supply Applications $\left(\mathrm{V}^{+}=5.0 \mathrm{~V}_{\mathrm{DC}}\right)$ (Continued)
DC Coupled Low-Pass RC Active Filter

$f_{O}=1 \mathrm{kHz}$
$\mathrm{Q}=1$
$\mathrm{~A}_{\mathrm{V}}=2$

High Input Z, DC Differential Amplifier


Typical Single-Supply Applications ( $\mathrm{V}^{+}=5.0 \mathrm{~V}_{\mathrm{DC}}$ ) (Continued)


00929928

If R1 $=$ R5 \& R3 $=R 4=R 6=R 7$ (CMRR depends on match)

$$
V_{O}=1+\frac{2 R 1}{R 2}\left(V_{2}-V_{1}\right)
$$

As shown $V_{O}=101\left(V_{2}-V_{1}\right)$

Using Symmetrical Amplifiers to Reduce Input Current (General Concept)


00929929

Typical Single-Supply Applications $\left(\mathrm{V}^{+}=5.0 \mathrm{~V}_{\mathrm{DC}}\right)$ (Continued)

$Q=25$
00929931
inches (millimeters) unless otherwise noted


MX S.O. Package (M)
Order Number LM324M, LM324MX, LM324AM, LM324AMX, LM2902M or LM2902MX
NS Package Number M14A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

$$
\frac{0.092}{(2.337)} \text { DIA } \frac{0.030}{(0.762)} \text { MEX } \text { DEPH }-
$$



OPTION 1


Molded Dual-In-Line Package (N) Order Number LM324N, LM324AN or LM2902N NS Package Number N14A


Ceramic Flatpak Package
Order Number JL124ABDA, JL124ABZA, JL124ASDA, JL124BDA, JL124BZA, JL124SDA, LM124AW/883, LM124AWG/883, LM124W/883 or LM124WG/883

NS Package Number W14B
LM124/LM224/LM324/LM2902 Low Power Quad Operational Amplifiers

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)


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$\left.\begin{array}{lllll}\hline \text { National Semiconductor } & \text { National Semiconductor } & \begin{array}{l}\text { National Semiconductor }\end{array} & \begin{array}{l}\text { National Semiconductor } \\ \text { Americas Customer }\end{array} & \text { Europe Customer Support Center }\end{array} \quad \begin{array}{l}\text { Asia Pacific Customer }\end{array}\right]$

# KBBC SERIES MICROPROCESSOR CONTROLLED BATTERY POWERED DC/DC Variable Speed Motor Control 

for 12, 24, 36 and 48 Volt PM and Series Wound DC Motors thru 2HP Continuous Duty and 4HP Peak Duty<br>TYPICAL APPLICATIONS<br>- Scooters • Personnel Carriers • Carts • Electric Boats<br>- Portable Pumps • Lifts • Floor Polishers



## STANDARD FEATURES

- High Frequency PWM Operation: Reduces motor noise and increases efficiency.
- Controlled Acceleration and Deceleration: Provides timed acceleration to set speed and deceleration to zero speed.
- Diagnostic LEDs: Provide indication of power on (PWR ON) and control status (STATUS).
- Built-In Reversing Contactor: Provides forward/reverse operation with a low power reversing switch or with a center-off throttle potentiometer (wigwag).
- Run Relay: Used to turn on or off equipment or signal a warning if a fault has occurred.
- Brake Driver Circuit: Powers an optional electromechanical brake (current regulated and short circuit protected).
- Key Switch Operation with Built-In Battery Power Contactor: Allows the use of a low power switch to turn control on and off.
- Inhibit Circuit: Allows control to be turned off electronically with a separate low power switch.
- Latching Circuit: Allows momentary switches to start, stop, and reverse the control.
- Limit Switch Circuit (Stop Forward and Stop Reverse): Allows limit switches to be used to immediately stop the control in forward or reverse directions.
- Single-Ended or Wigwag Potentiometer Control: Allows the Main Speed Potentiometer to be used as single-ended (zero speed is at $0 \%$ rotation) or wigwag (zero speed is at $50 \%$ rotation).


## PROTECTIVE FEATURES

- Electronic Current Limit: Protects the motor and control against overload.
- Polarity Protected: Prevents control damage if the battery is wired incorrectly.
- Short Circuit Protected: Protects main power transistor from failure due to a short at the motor.
- Overtemperature Protection: Reduces control output as the transistors reach maximum operating temperature.
- Overvoltage Protection: Will turn off the control if the battery voltage exceeds $125 \%$ of nominal.
- Undervoltage Protection: Will turn off the control if battery voltage reduces below $65 \%$ of nominal.


## SAFETY FEATURES

- Potentiometer Fault Circuit: Turns the control off if a short, open, or ground occurs at the potentiometer.
- High Pedal Disable Function: Prevents control startup until the potentiometer returns to zero.


## DESCRIPTION

The KBBC series of battery powered variable speed controls are designed for $12,24,36$, and 48 Volt PM and Series Wound DC motors. Microcontroller design provides superior performance and ease of tailoring to specific applications. Operating in a regenerative mode, precise and efficient control is obtained using state-of-the-art MOSFET technology. The KBBC operates at a switching frequency of 16 kHz , which provides high motor efficiency and quiet operation.

The KBBC contains many standard features such as current limit, short circuit protection, speed potentiometer fault detector, overtemperature sensing, and undervoltage/overvoltage protection. A variety of trimpots are provided, which can be used to tailor the control to exact specifications. The control also contains LEDs that indicate "power on" and "status." A DC power contactor allows a low power switch to turn the control on and off. Reversing contactors provide arcless forward, stop, and reverse operation. In addition, a brake driver circuit is used to power an optional electromagnetic brake.

The KBBC can be controlled in several ways, such as singleended or wigwag speed potentiometer and 0-5 Volts DC signal following. The controls contain a built-in heat sink that also serves as a mounting base.

## TRIMPOT ADJUSTMENTS

- Timed Brake Delay (T-BRK): Sets the delay time before the brake is engaged.
- Current Limit (CL): Sets the current limit (overload), which limits the maximum current to the motor.
- IR Compensation (IR): Sets the amount of compensating voltage required to keep the motor speed constant under changing loads.
- Deceleration (DECEL): Sets the amount of time for the motor to decelerate from the set speed to zero speed.
- Acceleration (ACCEL): Sets the amount of time for the motor to accelerate from zero speed to the set speed.
- Minimum Speed (MIN): Sets the minimum motor speed.
- Reverse Maximum Speed (RMAX): Sets the maximum motor speed in the reverse direction (a \% of FMAX setting).
- Forward Maximum Speed (FMAX): Sets the maximum motor speed in the forward direction.

GENERAL PERFORMANCE SPECIFICATIONS

| Parameter | Specification | Factory Setting |
| :---: | :---: | :---: |
| Input Voltage Range (\% Nominal) | 75-125 | 100 |
| Intermittent Duty Operation (Minutes) | 2 | - |
| Peak Duty Operation (Seconds) | 7 | - |
| Overvoltage Shutdown (\% Nominal Input Voltage) | 125 | - |
| Undervoltage Warning (\% Nominal Input Voltage, $\pm 10 \%$ ) | 85 | - |
| Undervoltage Shutdown (\% Nominal Input Voltage) | 65 | - |
| Nominal Carrier Frequency (kHz) | 16 | - |
| Electromagnetic Brake Delay Trimpot (T-BRK) Range (Seconds) | 0.2-2.5 | 1 |
| CL Trimpot (CL) Range (\% Range Setting) | 0-200 | 150 |
| IR Compensation Trimpot (IR) Range (\% Nominal Battery Voltage) | 0-25 | 4 |
| Acceleration Trimpot (ACCEL) Range (\% Base Speed) | 0.1-15 | 2 |
| Deceleration Trimpot (DECEL) Range (\% Base Speed) | 0.1-15 | 2 |
| Minimum Trimpot (MIN) Range (\% Base Speed) | 0-30 | 0 |
| Forward Maximum Speed Trimpot (FMAX) Range (\% Base Speed)* | 60-100 | 100 |
| Reverse Maximum Speed Trimpot (RMAX) Range (\% Forward Maximum Speed) | 50-100 | 100 |
| Electromagnetic Brake Current Rating (Amps DC) | 1 | - |
| Heat Sink Overtemperature Protection Point ( ${ }^{\circ} \mathrm{C}$ ) | 100 | - |
| Deadband in Wigwag Throttle Mode (Volts DC) | $\pm 0.3$ | - |
| Wigwag Throttle Signal Input Voltage for Maximum Forward (Volts DC) | $2.5-5.0$ | 5 |
| Wigwag Throttle Signal Input Voltage for Neutral (Volts DC) | 1.2-2.5 | 2.5 |
| Wigwag Throttle Signal Input Voltage for Maximum Reverse (Volts DC) | 0 | 0 |
| Single Ended Throttle Signal Range for Full Speed Forward or Reverse (Volts DC) | $0-2.5$ to 5.0 | 0-5 |
| Timed Current Limit (TCL) Trip Time (Seconds) | 7 | - |
| Run Relay Output Contact Rating (Amps at 30 Volts DC, Amps at 125 Volts AC) | 1, 0.5 | - |
| Auxiliary Power Connector (P2) Rating (Maximum Amps DC) | 10 | - |
| Operating Temperature Range ( ${ }^{\circ} \mathrm{C}$ ) | 0-45 | - |

*FMAX trimpot is also used as an input/output gain potentiometer.
ELECTRICAL RATINGS

| Model No. | Part No. | Nominal Battery Voltage (Volts DC) | Nominal Motor Voltage (Volts DC) | Continuous Duty |  | Intermittent Duty (2 Minutes) |  | Peak Duty (7 Seconds) |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | $\begin{gathered} \hline \text { Maximum } \end{gathered}$ | $\begin{gathered} \text { Amps } \\ \text { DC } \end{gathered}$ | $\begin{gathered} \text { Maximum } \\ \mathrm{HP} \end{gathered}$ | Amps DC | $\begin{gathered} \hline \text { Maximum } \\ \mathrm{HP} \end{gathered}$ | $\begin{gathered} \text { Amps } \\ \text { DC } \end{gathered}$ |
| KBBC-24M | 9500 | 12 | 0-12 | 1/2 | 40 | 3/4 | 60 | 1 | 80 |
|  |  | 24 | 0-24 | 1 | 40 | 11/2 | 60 | 2 | 80 |
| KBBC-44M | 9501 | 12 | 0-12 | 1/2 | 40 | 3/4 | 60 | 1 | 80 |
|  |  | 24 | 0-24 | 1 | 40 | $11 / 2$ | 60 | 2 | 80 |
|  |  | 36 | 0-36 | 11/2 | 40 | 2 | 60 | 3 | 80 |
|  |  | 48 | 0-48 | 2 | 40 | 3 | 60 | 4 | 80 |

Note: Custom units are available with various voltages and currents with or without DC Power Contactor or Reversing Contactor.

## JUMPER SELECTABLE FEATURES

[^3]

Notes: 1. If Key Switch is not used, a connection must be made between the red wire of Connector P1 and quick-connect
Terminal B+ for the control to operate.
2. RUN FWD and RUN REV Direction Switches are not used in wigwag operation.
3. Customer supplied.

VOLTAGE FOLLOWING CONNECTION


ENABLE SWITCH CONNECTION



GREEN AND RED STATUS LEDs

| Control Status | Green LED | Red LED | Flash Rate* |
| :---: | :---: | :---: | :---: |
| Run | On | Off | Slow |
| Stop | On | Off | Quick |
| Curent Limit (Warning) | Off | On | Steady |
| Undervoltage (Warning) | On | On | Slow |
| Overvoltage/Undervoltage Fault (Shutdown) | On | On | Quick |
| Overtemperature Fault (Shutdown) | On | On | Slow Alternating |
| Main Speed Potentiometer Fault (Shutdown) | On | On | Quick Alternating |
| Motor or Brake Fault (Shutdown) | On | On | Double Quick Alternating |
| Timed Current Limit (Shudown) | Off | On | Quick |

*Flash Rate: Slow $=1$ second on $/ 1$ second off. Quick $=0.15$ second on $/ 0.15$ second off.

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[^0]:    

[^1]:    $\mathrm{C}_{1}=$ Required if regulator is distant from filter capacitors.
    $\mathrm{C}_{\mathrm{L}}=$ Regulator is stable with no load capacitor into resistive loads.

[^2]:    A1 = LM101A, LM201A, LM301A.
    $\mathrm{Cl}=2 \mu \mathrm{~F}$ optional - improves ripple rejection, noise and transient response.

[^3]:    - JA - Battery Voltage (VOLTAGE - 12/24/36/48): Selects nominal battery voltage.

    JB - Motor Current (CURRENT - 10A/20A/30A/40A): Selects nominal motor current.

    - J1 - Signal Type (SIG - VF/POT): Selects voltage following or potentiometer operation.
    - J2 - Speed Potentiometer Mode (SPD - SE/WW): Selects single-ended or wigwag speed control.
    - J3 - Current Limit Mode (TCL - NTCLTCL): Selects non-timed current limit or timed current limit.
    - J4 - High Pedal Mode (HPD - NHPD/HPD): Selects non-high pedal disable or high pedal disable.
    - J5 - Deceleration Mode (STP - DEC/FIX): Selects adjustable or fixed ( 0.1 second) deceleration when a stop command is given.
    - J6-Direction Switch Type (LATCH - OFF/ON): Selects maintained or momentary direction commands.
    - J7-Cycling Mode (CYCL - OFF/ON): Selects cycling of relay which is used to brake the motor.
    - J8 - Relay Output Contacts (RLY - NO/NC): Selects normally open or normally closed Run Relay contacts.

