

## Universitas Indonesia Library >> Buku Teks

**Judul:**

Circuits, interconnections, and packaging for VLSI / H.B. Bakoglu

**Pengarang/Penulis:**

Bakoglu, H. B.

**Subjek:**

Integrated circuits-VLSI; Design and construction; Integrated circuits -  
-Very large scale integration --Design and construction; Electronic  
packaging

**Nomor Panggil:**

621.381 73 BAK c

**Penerbitan:**

Addison-Wesley

**Link Terkait:**

- [Deskripsi Bibliografi](#)
- [Dokumen Yang Mirip](#)
- [Universitas Indonesia Library](#)