

***Judul:***

Languages, design methods, and tools for electronic system design:  
selected contributions from FDL 2017

***Pengarang/Penulis:***

***Subjek:***

Formal methods (Computer science) -- Congresses; Software engineering --  
Congresses; Computer software -- Verification -- Congresses; Software  
architecture -- Congresses; Computer hardware description languages --  
Congresses

***Nomor Panggil:***

e20509145

***Penerbitan:***

Springer Nature

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