

Judul:

Design automation techniques for approximation circuits: Verification, synthesis and test

Pengarang/Penulis:

Chandrasekharan, Arun, author

Subjek:

Computing systems; Design--Automation.; Approximate computing

Nomor Panggil:

e20502847

Penerbitan:

Springer Cham

Link Terkait:

- [Deskripsi Bibliografi](#)
- [Abstrak](#)
- [Dokumen Yang Mirip](#)
- [Universitas Indonesia Library](#)